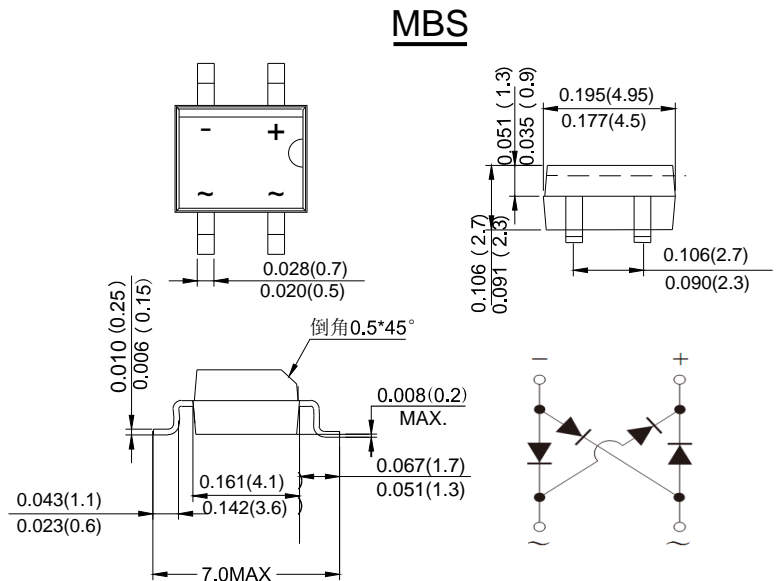


Features

- Glass Passivated Die Construction
- Low leakage
- Ideal for printed circuit board
- Surge overload rating-35A peak
- Designed for Surface Mount Application
- Plastic Material-UL Flammability 94V-0

Mechanical Data

- Case:Reliable low cost construction utilizing molded plastic technique
- Terminals:Plated Leads Solderable per MIL-STD-202,Method208
- Polarity:As Marked on Case
- Mounting Position:Any
- Marking:Type Number



dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Single Phase, half wave, 60Hz, resistive or inductive load.

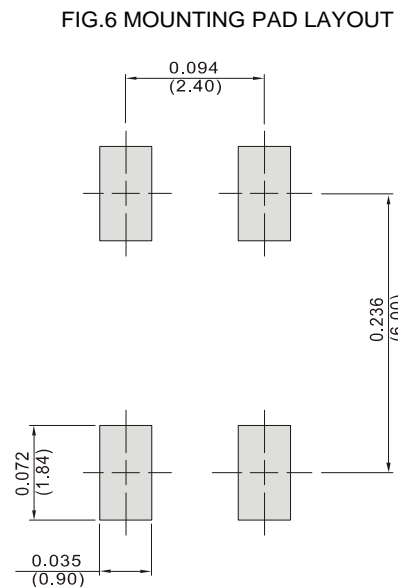
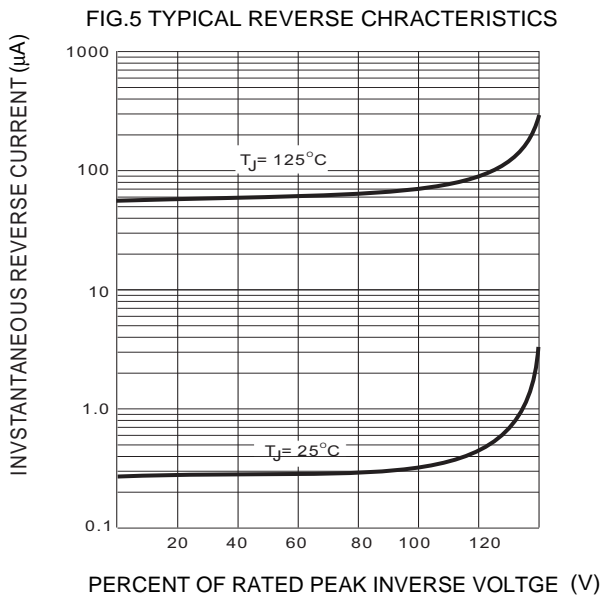
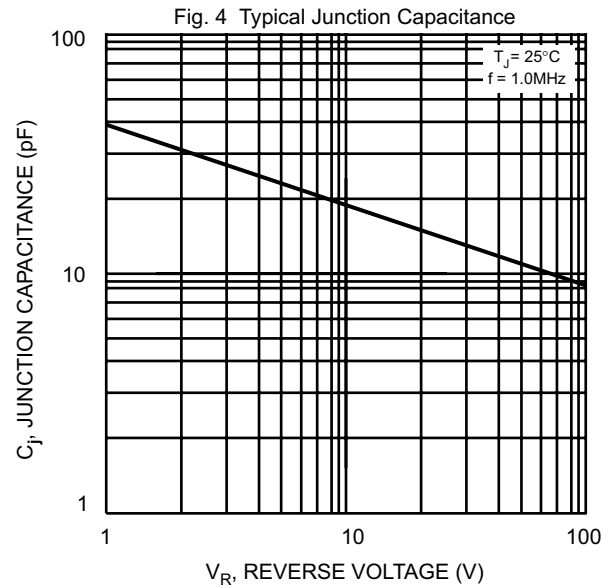
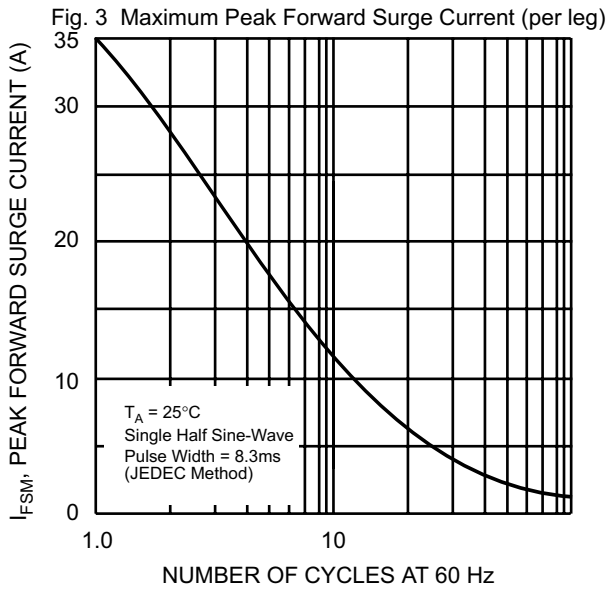
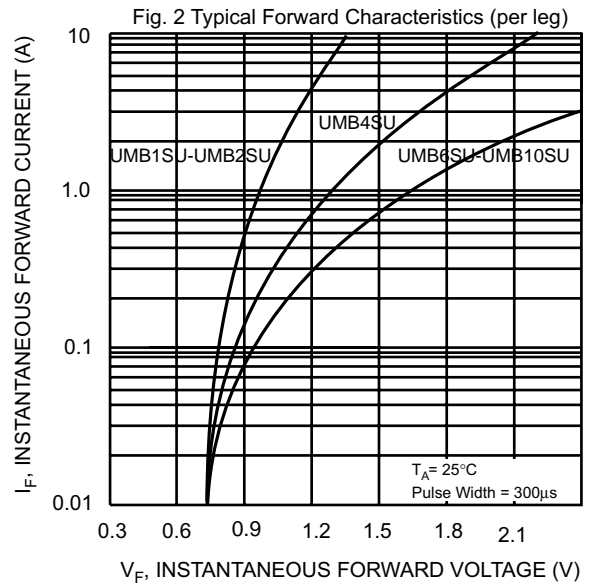
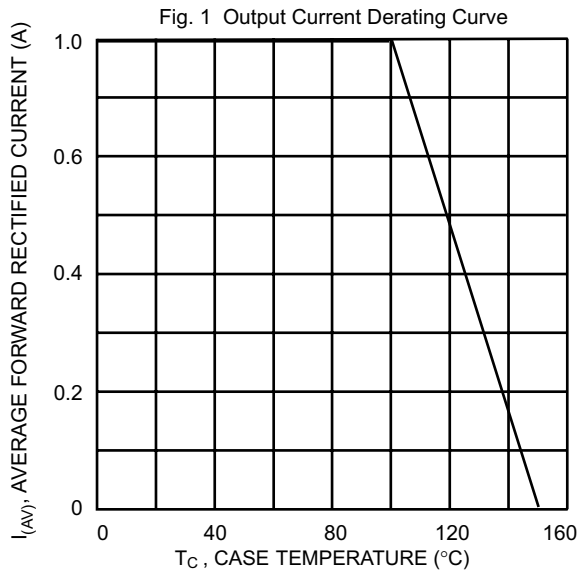
For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOL	UMB1SU	UMB2SU	UMB4SU	UMB6SU	UMB8SU	UMB10SU	UNITS
Peak Repetitive Reverse Voltage	V_{RRM}							
Working Peak Reverse Voltage	V_{RWM}	100	200	400	600	800	1000	V
DC Blocking Voltage	V_{DC}							
RMS Reverse Voltage	V_{RMS}	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1)@T _c =100°C	IF(AV)	1.0						A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	35						A
I ² t Rating for Fusing (t < 8.3ms)	I ² t	5.084						A ² s
Forward Voltage per element @IF=1.0A	V _{FM}	1.0	1.3	1.7				V
Peak Reverse Current @T _A =25°C At Rated DC Blocking Voltage @T _A =125°C	I _R	5.0 200						uA
Maximum reverse recovery time (Note 2)	T _{RR}	50			75			ns
Typical Junction Capacitance per leg (Note 3)	C _J	13						pF
Typical Thermal Resistance per leg	R _{θJA}	60						°C/W
	R _{θJL}	16						
Operating and Storage Temperature Range	T _J ,T _{STG}	-55to+150						°C

Note:1. Mounted on glass epoxy PC board with 1.3mm² solder pad.

2. Reverse Recovery Test Conditions: IF=0.5A, IR=1A, Irr=0.25A.

3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.



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