**NCE85H21** 

#### NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE85H21 uses advanced trench technology and design to provide excellent  $R_{\text{DS(ON)}}$  with low gate charge. It can be used in automotive applications and a wide variety of other applications.

#### **General Features**

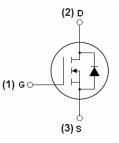
- $V_{DSS} = 85V, I_D = 210A^{(Note5)}$  $R_{DS(ON)} < 3.8m\Omega @ V_{GS} = 10V$
- Good stability and uniformity with high E<sub>AS</sub>
- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

#### **Application**

- Automotive applications
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



#### Schematic diagram



#### Marking and pin assignment



TO-220-3L top view

#### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE85H21	NCE85H21	TO-220	-	-	-

#### Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DSS</sub>	85	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
Drain Current-Continuous	I <sub>D</sub>	210 <sup>(Note5)</sup>	А
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	150	Α
Pulsed Drain Current	I <sub>DM</sub>	850	Α
Maximum Power Dissipation	P <sub>D</sub>	310	W
Derating factor		2.07	W/℃



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# NCE85H21

Single pulse avalanche energy (Note 3)	E <sub>AS</sub>	2200	mJ	
Peak Diode Recovery dv/dt (Note 4)	dv/dt	5	V/ns	
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 175	$^{\circ}$ C	

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case (Note 1)	$R_{ heta JC}$	0.48	°C/W
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#### Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	85	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =85V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±200	nA
On Characteristics						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	2	3	4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =10V, $I_D$ =40A	-	3.2	3.8	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =25V,I <sub>D</sub> =40A	100	165	-	S
Dynamic Characteristics			•			
Input Capacitance	C <sub>lss</sub>	\/_05\/\/_0\/	-	11000	-	PF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =25V,V <sub>GS</sub> =0V, F=1.0MHz	-	914	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.UIVID2	-	695	-	PF
Switching Characteristics						
Turn-on Delay Time	t <sub>d(on)</sub>	\/ -20\/ L -40A	-	23	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =38V, $I_{D}$ =40A $V_{GS}$ =10V, $R_{GEN}$ =1.2 $\Omega^{(Note2)}$	-	190	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	V <sub>GS</sub> =10V,R <sub>GEN</sub> =1.2Ω	-	130	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	120	-	nS
Total Gate Charge	Qg	\/ -60\/   -40 \	-	250	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =60V, $I_D$ =40A, $V_{GS}$ =10V <sup>(Note2)</sup>	-	48	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	VGS-10V	-	98	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =40A	-	-	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF = 40A	-	63	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note2)}$	-	98	-	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

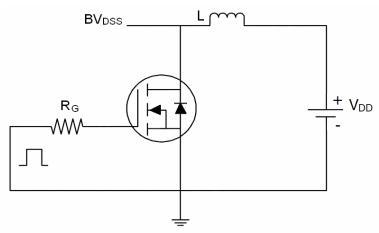
#### Notes:

- 1. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 2. Pulse Test: Pulse Width  $\leq$  400 $\mu$ s, Duty Cycle  $\leq$  2%.
- 3. EAS condition: Tj=25  $^{\circ}\text{C}$  ,V  $_{DD}$  =42.5 V ,V  $_{G}$  =10 V ,L=0.5 mH ,Rg=25  $\Omega$  ,I  $_{AS}$  =37 A
- 4. ISD $\leq$ 125A, di/dt $\leq$ 260A/ $\mu$ s, VDD $\leq$ V(BR)DSS, TJ  $\leq$ 175°C
- 5. Package limitation current is 190A.

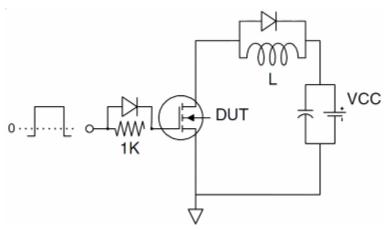


#### **Test Circuit**

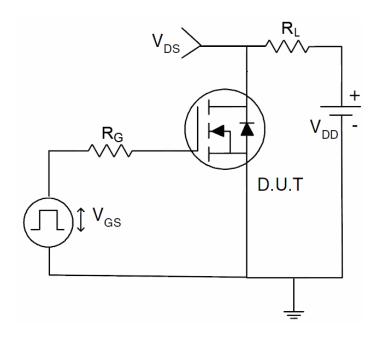
# 1) E<sub>AS</sub> test Circuit



## 2) Gate charge test Circuit

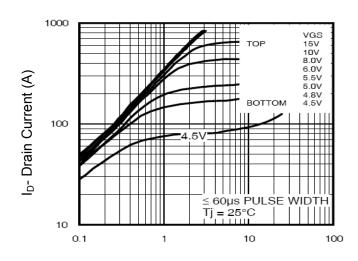


#### 3) Switch Time Test Circuit



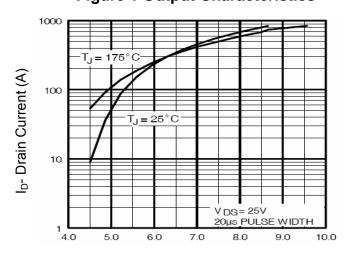


### **Typical Electrical and Thermal Characteristics**



Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics



Vgs Gate-Source Voltage (V)

**Figure 2 Transfer Characteristics** 

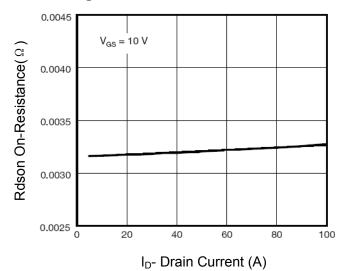
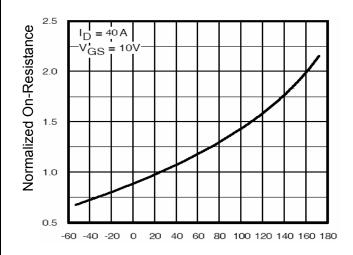


Figure 3 Rdson- Drain Current



T<sub>J</sub>-Junction Temperature(°C)

Figure 4 Rdson-JunctionTemperature

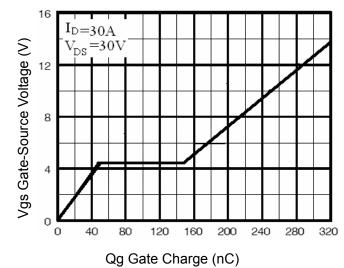


Figure 5 Gate Charge

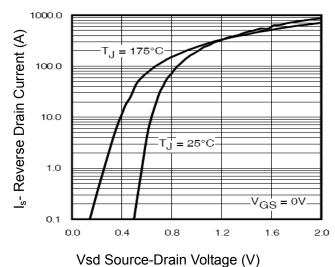


Figure 6 Source- Drain Diode Forward



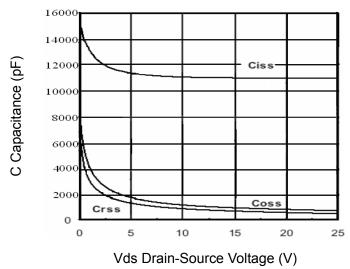
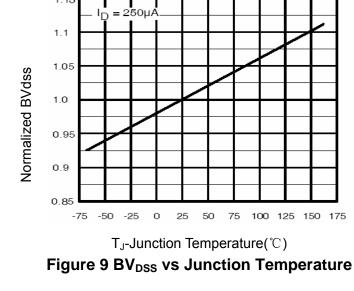
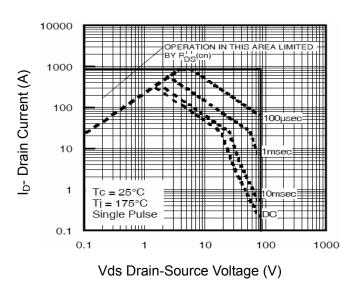


Figure 7 Capacitance vs Vds





**Figure 8 Safe Operation Area** 

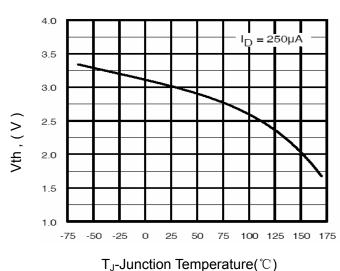
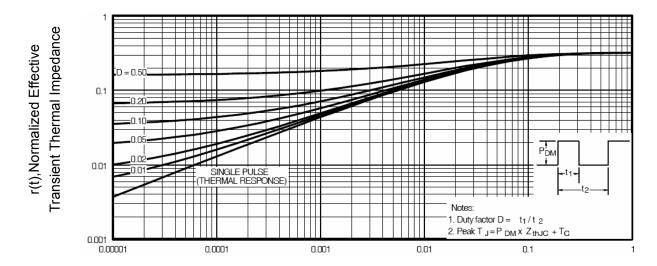


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

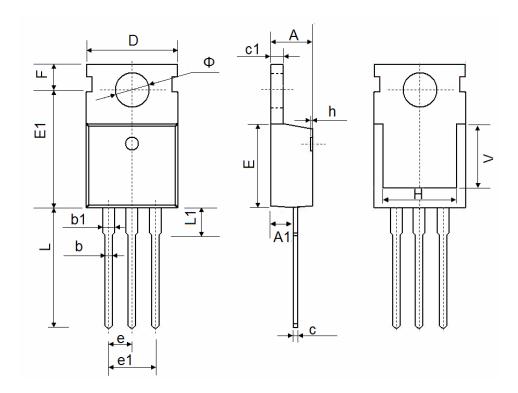


Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

**Pb Free Product** 

# **TO-220-3L Package Information**



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
E	8.9500	9.750	0.352	0.384	
E1	12.650	12.950	0.498	0.510	
е	2.540	TYP.	0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	7.500	REF.	0.295	REF.	
Ф	3.400	3.800	0.134	0.150	



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