

___D2

🗌 S2

__ \$2

___G2

D 1

D1 🖂

S1 🖂

S1 📺

G1 🖂

G2 **S**2 **S**2 D2

Schematic diagram

8205B

Marking and pin assignment

Tssop-8 top view

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE8205B uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

- V_{DS} = 20V,I_D = 6.5A $R_{DS(ON)} < 27m\Omega @ V_{GS}=2.5V$
 - $R_{DS(ON)} < 22m\Omega @ V_{GS}=4.5V$
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- Battery protection
- Load switch
- Power management

Package Marking and Ordering Information					
Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
8205B	NCE8205B	TSSOP-8	Ø330mm	12mm	3000 units

Absolute Maximum Ratings (T_A=25[°]C unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	Vds	20	V	
Gate-Source Voltage	Vgs	±12	V	
Drain Current-Continuous	Ι _D	6.5	A	
Drain Current-Pulsed (Note 1)	I _{DM}	25	A	
Maximum Power Dissipation	PD	1.5	W	
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C	

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ extsf{ heta}JA}$	83	°C/W
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Electrical Characteristics (T_A=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	20	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =20V, V_{GS} =0V	-	-	1	μA



Pb Free Product

NCE8205B

Gate-Body Leakage Current	I _{GSS}	V_{GS} =±12V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)					L	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	0.5	0.7	1.2	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =4.5A	-	15	22	mΩ
Drain-Source On-State Resistance		V _{GS} =2.5V, I _D =3.5A	-	19	27	mΩ
Forward Transconductance	g fs	V _{DS} =5V,I _D =4.5A	-	10	-	S
Dynamic Characteristics (Note4)		·		•		
Input Capacitance	C _{lss}		-	900	-	PF
Output Capacitance	Coss	V _{DS} =10V,V _{GS} =0V, F=1.0MHz	-	220	-	PF
Reverse Transfer Capacitance	C _{rss}		-	100	-	PF
Switching Characteristics (Note 4)		·		•		
Turn-on Delay Time	t _{d(on)}		-	10	20	nS
Turn-on Rise Time	tr	V _{DD} =10V,I _D =1A V _{GS} =4.5V,R _{GEN} =6Ω	-	11	25	nS
Turn-Off Delay Time	t _{d(off)}		-	35	70	nS
Turn-Off Fall Time	t _f		-	30	60	nS
Total Gate Charge	Qg	V _{DS} =10V,I _D =6A,	-	12	15	nC
Gate-Source Charge	Q _{gs}		-	2.3	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =4.5V	-	1	-	nC
Drain-Source Diode Characteristics			•		•	
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =1.7A	-	0.75	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	6.5	А

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production







Typical Electrical and Thermal Characteristics

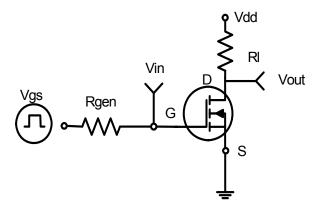
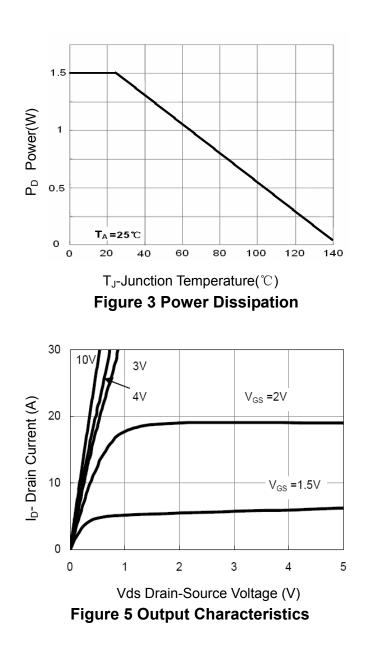


Figure 1:Switching Test Circuit



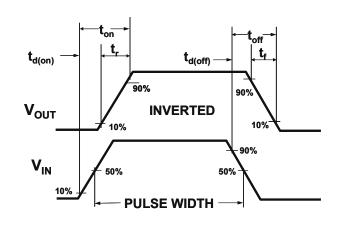
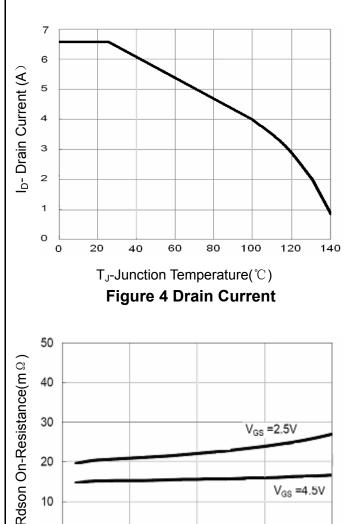


Figure 2:Switching Waveforms



10

15

0

0

5

20





75

10

100

125

15

T.,=25℃

1

,=-55℃

0.8

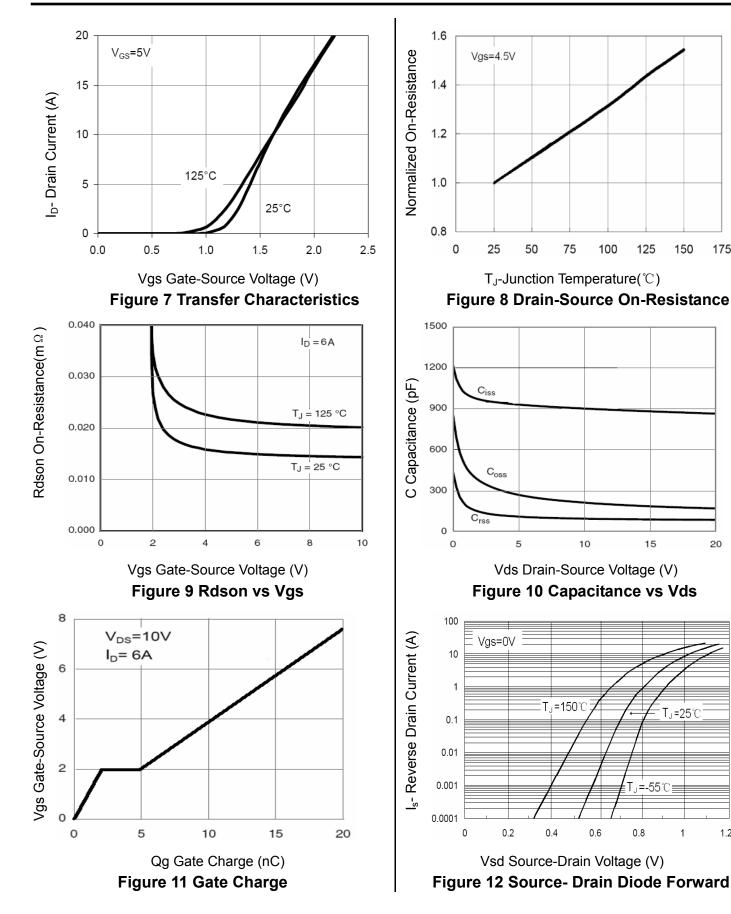
0.6

150

175

20

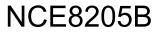
NCE8205B



1.2







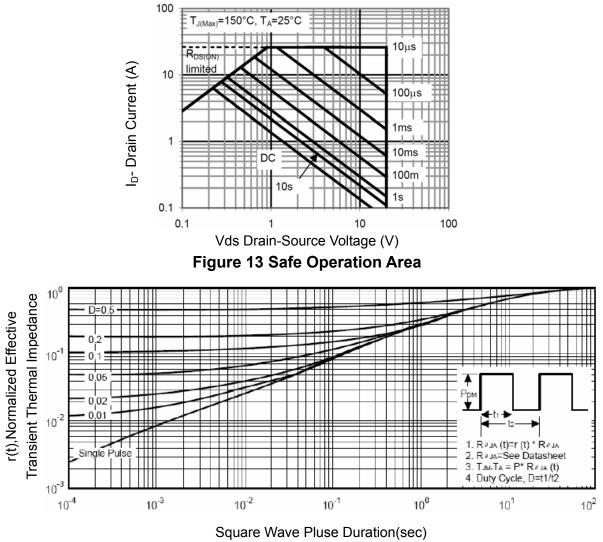
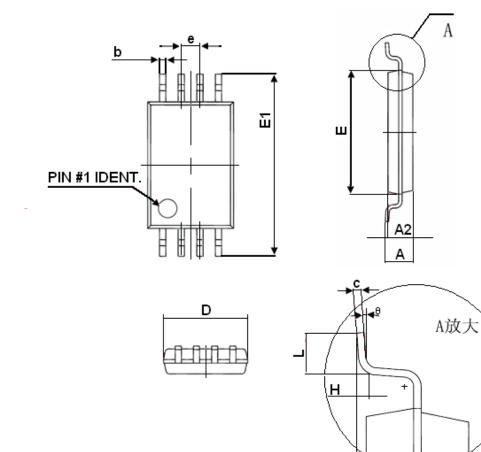


Figure 14 Normalized Maximum Transient Thermal Impedance





Tssop-8 Package Information



Symbol	Dimensions In Millimeters			
	Min	Мах		
D	2.900	3.100		
E	4.300	4.500		
b	0.190	0.300		
С	0.090	0.200		
E1	6.250	6.550		
Α		1.100		
A2	0.800	1.000		
A1	0.020	0.150		
е	0.65(BSC)			
L	0.500	0.700		
Н	0.25(TYP)			
Θ	1°	7°		

A1





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