NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE75H26T uses advanced trench technology and design to provide excellent $R_{\text{DS(ON)}}$ with low gate charge. It can be used in automotive applications and a wide variety of other applications.

General Features

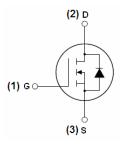
- V_{DSS} =75V, I_D =260A $R_{DS(ON)} < 3m\Omega$ @ V_{GS} =10V (Typ: 2.1 mΩ)
- Good stability and uniformity with high E_{AS}
- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Application

- Automotive applications
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



Schematic diagram



Marking and pin assignment



TO-247 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE75H26T	NCE75H26T	TO-247	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDSS	75	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	260	А
Drain Current-Continuous(T _C =100°C)	I _D (100℃)	200	Α
Pulsed Drain Current	I _{DM}	1060	Α
Maximum Power Dissipation	P _D	385	W
Derating factor		2.57	W /℃
Single pulse avalanche energy (Note 3)	E _{AS}	2200	mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	13	V/ns



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NCE75H26T

Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	°C
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Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 1)	$R_{ heta JC}$	0.39	°C/W
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·					
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	75	86	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =75V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±200	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250\mu A$	2	3	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =40A	-	2.1	3	mΩ
Forward Transconductance	g FS	V _{DS} =25V,I _D =40A	260	-	-	S
Dynamic Characteristics						
Input Capacitance	C _{lss}	\/ 20\/\/ 0\/	-	15700	-	PF
Output Capacitance	C _{oss}	V_{DS} =30V, V_{GS} =0V, F=1.0MHz	-	2410	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIHZ	-	1240	-	PF
Switching Characteristics	·					
Turn-on Delay Time	t _{d(on)}	\/ 00\/ L 40A	-	17	-	nS
Turn-on Rise Time	t _r	V _{DD} =38V,I _D =40A	-	80	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{GEN} =1.2 Ω (Note2)	-	100	-	nS
Turn-Off Fall Time	t _f	(Note2)	-	62	-	nS
Total Gate Charge	Qg	V _{DS} =38V,I _D =160A,	-	160	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =36 V , I_D =160 A , V_{GS} =10 V (Note2)	-	35	-	nC
Gate-Drain Charge	Q _{gd}	VGS-10V(NOIe2)	-	55	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =40A	-	-	1.2	V
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 40A	-	52	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note2)}$	-	110	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

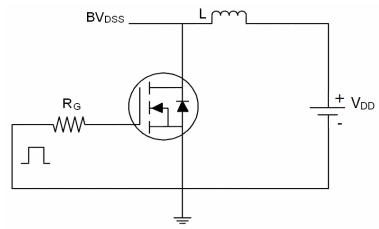
Notes:

- 1. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 2. Pulse Test: Pulse Width \leq 400 μ s, Duty Cycle \leq 2%.
- 3. EAS condition: Tj=25 $^{\circ}\text{C}$,VDD=37.5V,VG=10V,L=2mH,Rg=25 Ω ,IAS=37A
- 4. Isd \leqslant 125A, di/dt \leqslant 260A/ μ s, Vdd \leqslant V(BR)dss, TJ \leqslant 175°C

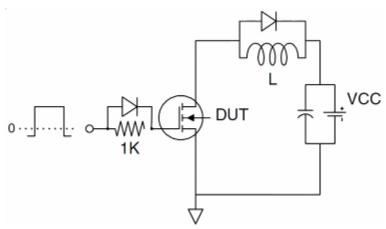


Test circuit

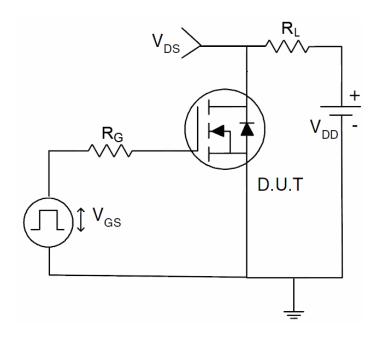
1) E_{AS} test Circuit



2) Gate charge test Circuit

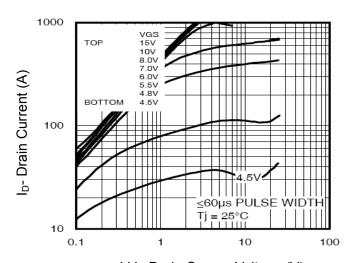


3) Switch Time Test Circuit



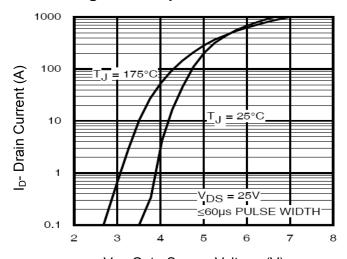


Typical Electrical and Thermal Characteristics



Vds Drain-Source Voltage (V)





Vgs Gate-Source Voltage (V)

Figure 2 Transfer Characteristics

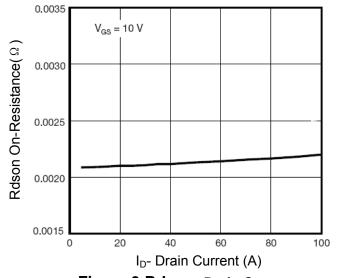
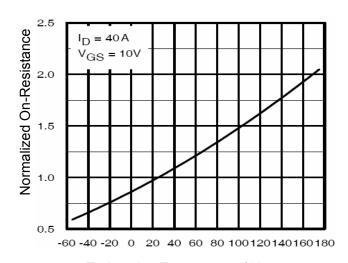


Figure 3 Rdson- Drain Current



T_J-Junction Temperature(°ℂ)

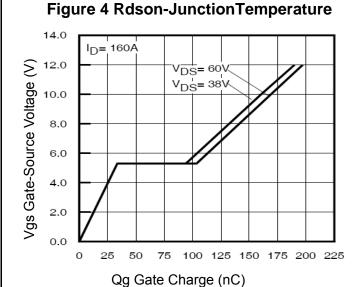


Figure 5 Gate Charge

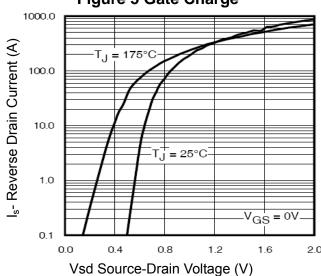


Figure 6 Source- Drain Diode Forward



10000

1000

100

10

0.1

Tj = 175°C Single Pulse

Ip- Drain Current (A)

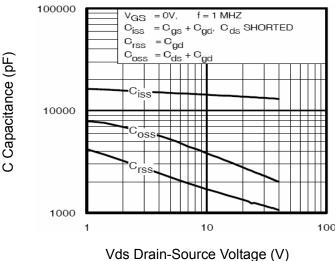


Figure 7 Capacitance vs Vds

OPERATION IN THIS AREA LIMITED BY RDS (on)

100



1000



10

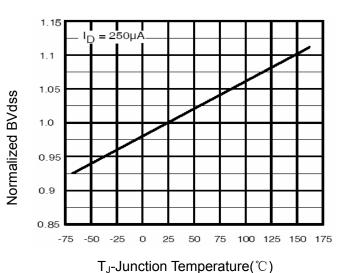
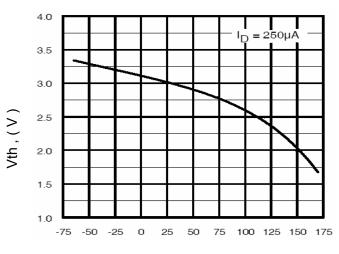


Figure 9 BV_{DSS} vs Junction Temperature



 $\label{eq:TJ-Junction Temperature} T_{J}-Junction Temperature (°C) $$ Figure 10 V_{GS(th)}$ vs Junction Temperature $$ T_{J}$-Junction Temperature $$ T$

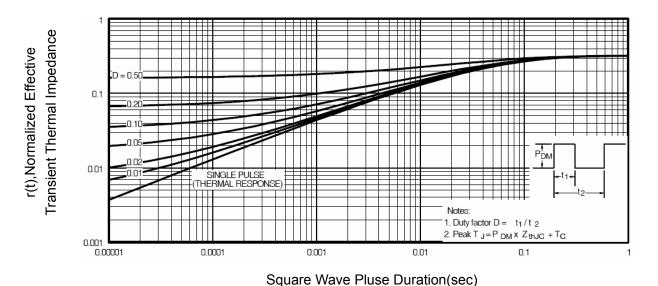
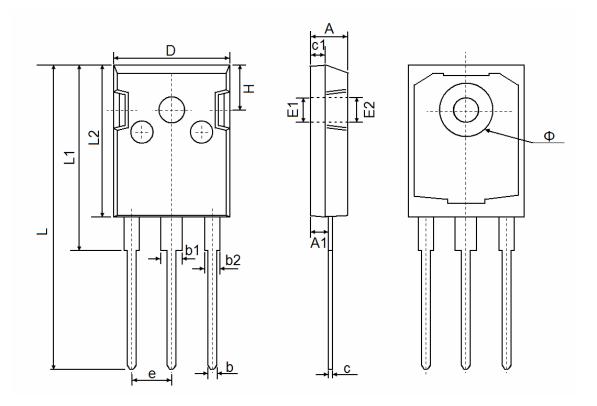


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-247 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches			
	Min.	Max.	Min.	Max.		
А	4.850	5.150	0.191	0.200		
A1	2.200	2.600	0.087	0.102		
b	1.000	1.400	0.039	0.055		
b1	2.800	3.200	0.110	0.126		
b2	1.800	2.200	0.071	0.087		
С	0.500	0.700	0.020	0.028		
c1	1.900	2.100	0.075	0.083		
D	15.450	15.750	0.608	0.620		
E1	3.500	REF	0.138 REF			
E2	3.600	3.600 REF		0.142 REF		
L	40.900	41.300	1.610	1.626		
L1	24.800	25.100	0.976	0.988		
L2	20.300	20.600	0.799	0.811		
Ф	7.100	7.300	0.280	0.287		
е	5.450) TYP	0.215 TYP			
Н	5.980 REF		0.235 REF			

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NCE75H26T

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