

NCE N-Channel Enhancement Mode Power MOSFET

General Description

The NCE7559k uses advanced trench technology and design to provide excellent $R_{\rm DS(ON)}$ with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.

Features

- V_{DS} =75V; I_{D} =59A@ V_{GS} =10V; $R_{DS(ON)}$ <8.5mΩ @ V_{GS} =10V
- Special process technology for high ESD capability
- Special designed for Convertors and power controls
- High density cell design for ultra low Rdson
- Fully characterized Avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

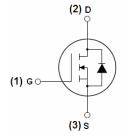
- Power switching application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply

Product Summary

BV _{DSS} typ.	84	>
R _{DS(ON)} typ.	7.2	mΩ
max.	8.5	mΩ
I _D	59	Α

100% UIS TESTED!





TO-252-2L top view

Schematic diagram

Package Marking and Ordering Information

	<u> </u>				
Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE7559K	NCE7559K	TO-252-2L	-	-	-

Table 1. Absolute Maximum Ratings (T_C=25℃)

Table 11 / Nocolate Maximum Natings (16-25 c)					
Parameter	Symbol	Value	Unit		
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	75	V		
Gate-Source Voltage (V _{DS} =0V)	V_{GS}	±20	V		
Drain Current (DC) at Tc=25℃	I _{D (DC)}	59	Α		
Drain Current (DC) at Tc=100°C	I _{D (DC)}	41	А		
Drain Current-Continuous@ Current-Pulsed (Note 1)	I _{DM (pluse)}	230	А		
Maximum Power Dissipation(Tc=25℃)	P _D	130	W		
Derating factor		0.87	W/℃		
Single pulse avalanche energy (Note 2)	E _{AS}	550	mJ		
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}\!\mathbb{C}$		

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2.EAS condition: Tj=25°C, VDD=37.5V, V_G=10V, L=0.5mH

Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R _{thJC}	1.15	°C/W
Thermal Resistance, Junction-to-Ambient (Maximum)	R _{thJA}	63	°C/W

Table 3. Electrical Characteristics (T_C=25 [°]C unless otherwise noted)

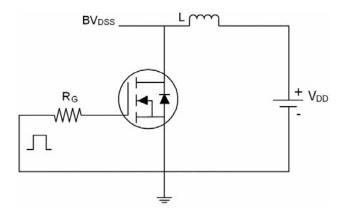
Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	75	84	-	V
Zero Gate Voltage Drain Current(Tc=25℃)	I _{DSS}	V _{DS} =75V,V _{GS} =0V	-	-	1	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I _{DSS}	V _{DS} =75V,V _{GS} =0V	-	-	10	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2	2.85	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =30A	-	7.2	8.5	mΩ
Dynamic Characteristics						
Forward Transconductance	g FS	V _{DS} =5V,I _D =30A	-	60	ı	S
Input Capacitance	C _{lss}	\/ - 25\/ \/ - 0\/	-	3400	ı	PF
Output Capacitance	Coss	V_{DS} =25V, V_{GS} =0V, F=1.0MHz	-	290	-	PF
Reverse Transfer Capacitance	C _{rss}	1 – 1.0IVII IZ	-	221	1	PF
Total Gate Charge	Q_g	V _{DS} =30V,I _D =30A,	-	94	Ī	nC
Gate-Source Charge	Q_{gs}	V _{DS} =30V,I _D =30A, V _{GS} =10V	-	16	Ī	nC
Gate-Drain Charge	Q_{gd}	VGS-10V	-	24	-	nC
Switching times						
Turn-on Delay Time	t _{d(on)}		-	15	Ī	nS
Turn-on Rise Time	t _r	V_{DD} =30 V , I_D =2 A , R_L =15 Ω	-	11	-	nS
Turn-Off Delay Time	$t_{d(off)}$	V_{GS} =10V, R_{G} =2.5 Ω	-	52	-	nS
Turn-Off Fall Time	t _f		-	13	-	nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I _{SD}		-	-	59	Α
Forward on voltage ^(Note 1)	V_{SD}	Tj=25℃,I _{SD} =40A,V _{GS} =0V	-	-	1.2	V
Reverse Recovery Time ^(Note 1)	t _{rr}	Tj=25℃,I _F =40A,di/dt=100A/μs	-	-	33	nS
Reverse Recovery Charge ^(Note 1)	Q _{rr}	1j-20 C,1f-40A,0l/0l-100A/µS	-	-	54	nC
Forward Turn-on Time	t _{on}	Intrinsic turn-on time is negligible(turn-on is dominated by L _S +L _D				y L _S +L _D)

Notes

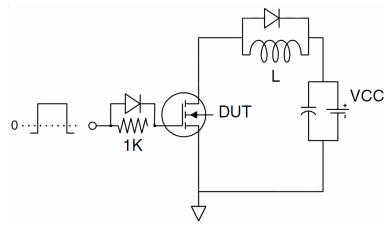
¹.Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 1.5%, R_G=25 Ω , Starting Tj=25 $^{\circ}$ C

Test circuit

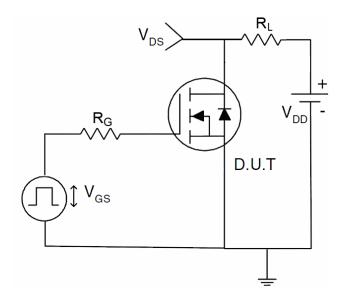
1) E_{AS} test circuit



2) Gate charge test circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (curves)

Figure 1. Safe operating area

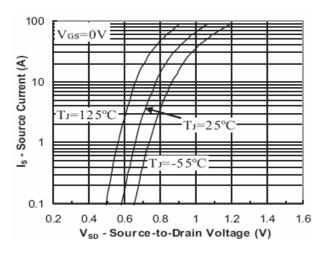


Figure3. Output characteristics

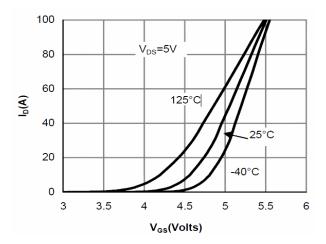


Figure 5. Static drain-source on resistance

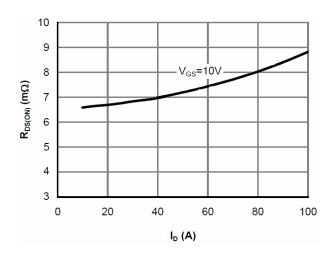


Figure 2. Source-Drain Diode Forward Voltage

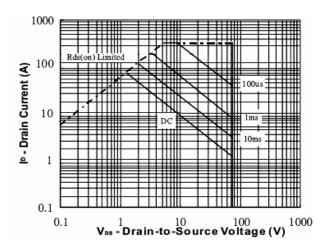


Figure 4. Transfer characteristics

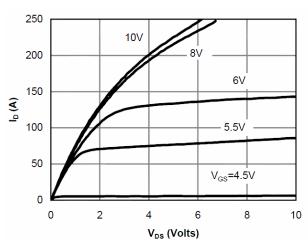
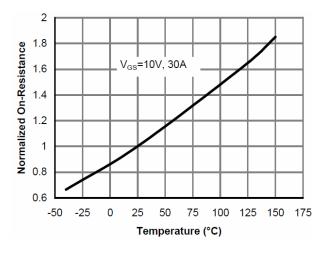


Figure 6. R_{DS(ON)} vs Junction Temperature





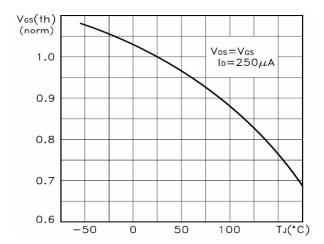


Figure9. Capacitance

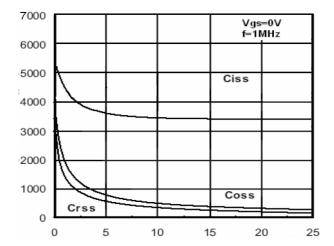


Figure 8. V_{GS(th)} vs Junction Temperature

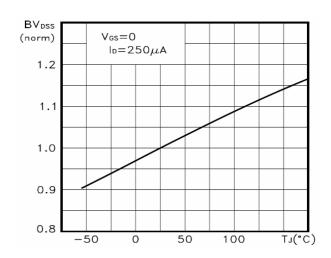


Figure 10. Gate charge waveforms

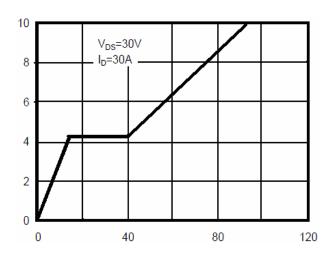
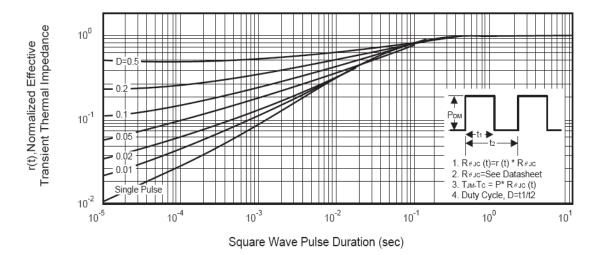
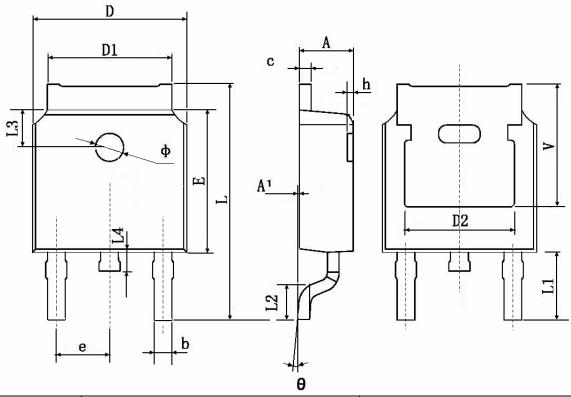


Figure 11. Normalized Maximum Transient Thermal Impedance



TO-252 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.83	4.830TYP.		TYP.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	2.900 TYP.		TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350 TYP.		0.211 TYP.		

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