

NCE7190

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE7190 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

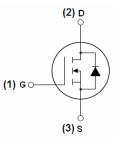
- $V_{DS} = 71V, I_D = 90A$ $R_{DS(ON)} < 6.8mΩ @ V_{GS} = 10V$ (Typ:5.9mΩ)
- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

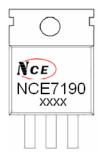
- Power switching application
- Hard switched and High frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% AVds TESTED!



Schematic diagram



Marking and pin assignment



TO-220-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE7190	NCE7190	TO-220-3L	-	-	-

Absolute Maximum Ratings (T_C=25[°]Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	71	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	90	А
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	63	Α
Pulsed Drain Current	I _{DM}	320	Α
Maximum Power Dissipation	P _D	170	W



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Derating factor		1.13	W/°C
Single pulse avalanche energy (Note 5)	E _{AS}	550	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{ heta Jc}$	0.88	°C/W
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Electrical Characteristics (Tc=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	71	74	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =71V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V		-	±100	nA
On Characteristics (Note 3)	<u>.</u>					
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	2	3	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =40A	-	5.9	6.8	mΩ
Forward Transconductance	g FS	V _{DS} =10V,I _D =40A	-	50	-	S
Dynamic Characteristics (Note4)						
Gate resistance	Rg	V _{DS} =0V,V _{GS} =0V,F=1.0MHz	-	0.63	-	Ω
Input Capacitance	C _{lss}	\/ -45\/\/ -0\/	-	4871	-	PF
Output Capacitance	Coss	V _{DS} =15V,V _{GS} =0V, F=1.0MHz	-	630.6	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIDZ	-	410.3	-	PF
Switching Characteristics (Note 4)	<u>.</u>					
Turn-on Delay Time	t _{d(on)}		-	36.1	-	nS
Turn-on Rise Time	t _r	V _{DD} =30V,I _D =42A	-	54.3	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{GEN} =10 Ω	-	85.2	-	nS
Turn-Off Fall Time	t _f		-	37.3	-	nS
Total Gate Charge	Qg	\/ -40\/ -040	-	85.7	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =48V, I_D =84A, V_{GS} =10V	-	23.2	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	31.2	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current (Note 2)	Is	-	-	-	90	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF =84A	-	88.3	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	65.9	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

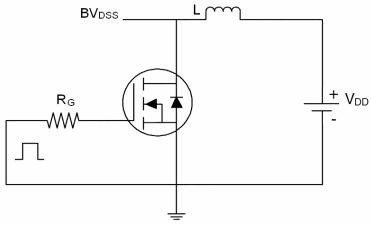
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ $300\mu s$, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^{\circ}\text{C}$,VDD=35V,VG=10V,L=0.5mH,Rg=25 Ω

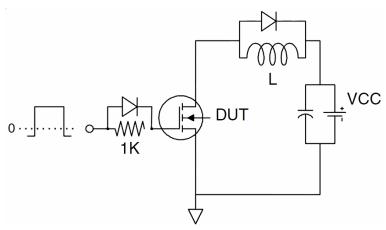
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Test Circuit

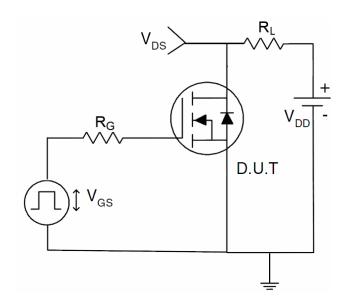
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

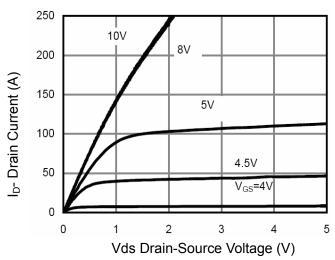


Figure 1 Output Characteristics

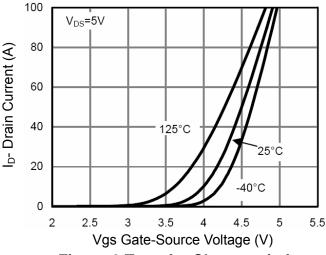


Figure 2 Transfer Characteristics

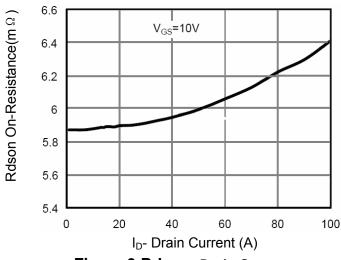


Figure 3 Rdson- Drain Current

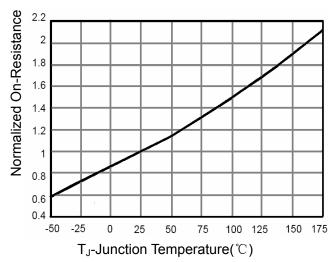


Figure 4 Rdson-JunctionTemperature

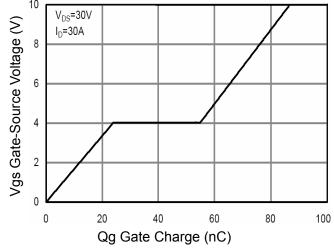


Figure 5 Gate Charge

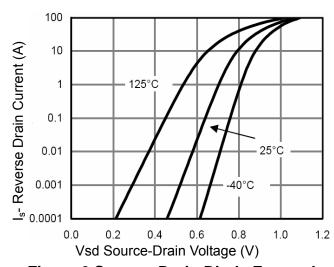


Figure 6 Source- Drain Diode Forward



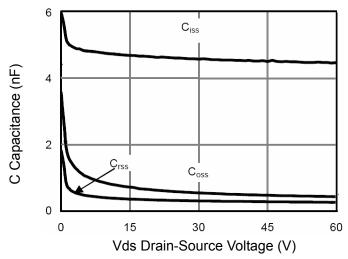


Figure 7 Capacitance vs Vds

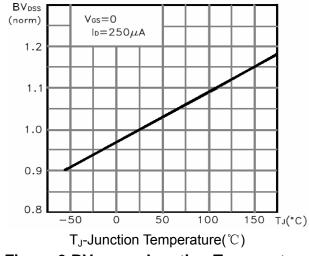


Figure 9 BV_{DSS} vs Junction Temperature

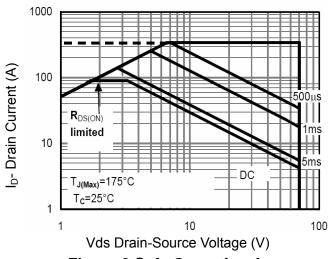


Figure 8 Safe Operation Area

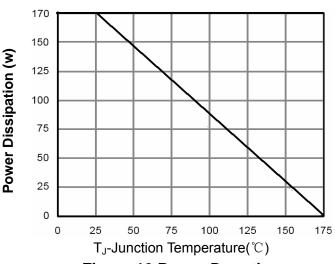
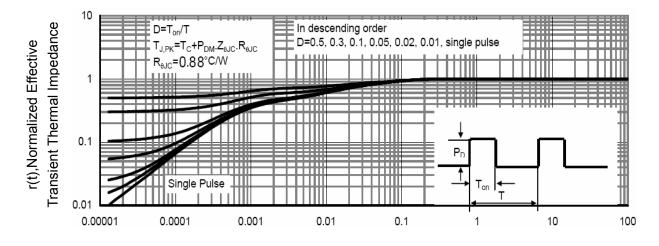


Figure 10 Power De-rating



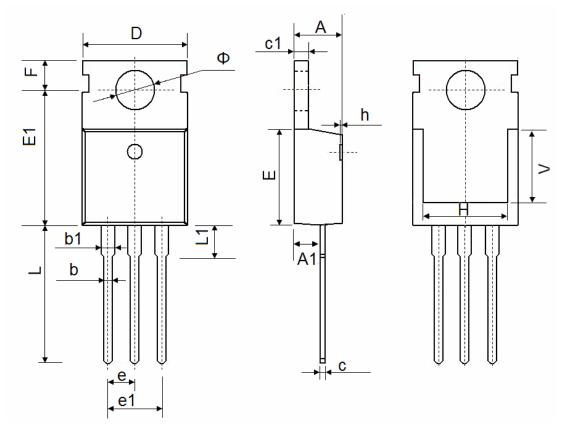
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

Pb Free Product



TO-220-3L Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
E	8.9500	9.750	0.352	0.384	
E1	12.650	12.950	0.498	0.510	
е	2.540	TYP.	0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	7.500	REF.	0.295 REF.		
Ф	3.400	3.800	0.134	0.150	



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