

## N and P-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE4606 uses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge . The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other applications.

#### **General Features**

#### N-Channel

 $V_{DS} = 30V, I_{D} = 6.5A$ 

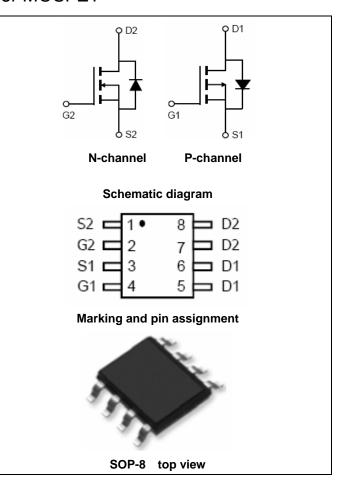
 $R_{DS(ON)}$  < 30m $\Omega$  @  $V_{GS}$ =10V

#### P-Channel

 $V_{DS} = -30V, I_{D} = -7A$ 

 $R_{DS(ON)}$  < 33m $\Omega$  @  $V_{GS}$ =-10V

- High power and current handing capability
- Lead free product is acquired
- Surface mount package



## **Package Marking and Ordering Information**

Device Marking I		Device	Device Package	Reel Size	Tape width	Quantity
	4606	NCE4606	SOP-8	Ø330mm	12mm	2500 units

# Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Paramete	Symbol	N-Channel	P-Channel	Unit	
Drain-Source Voltage	V <sub>DS</sub>	30	-30	V	
Gate-Source Voltage	V <sub>GS</sub>	±20	±20	V	
Continuous Drain Current	T <sub>A</sub> =25°C	1	6.5	-7	Α
Continuous Drain Current	T <sub>A</sub> =70°C	I <sub>D</sub>	5.4	-5.8	
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	30	-30	А
Maximum Power Dissipation T <sub>A</sub> =25℃		P <sub>D</sub>	2.0	2.0	W
Operating Junction and Storage Tem	$T_J, T_{STG}$	-55 To 150	-55 To 150	$^{\circ}$ C	

## **Thermal Characteristic**

Thermal Resistance,Junction-to-Ambient (Note2)	R <sub>0JA</sub>	N-Ch	62.5	°C/W
Thermal Resistance, Junction-to-Ambient (Note2)	$R_{\theta JA}$	P-Ch	62.5	°C/W





N-CH Electrical Characteristics (T<sub>A</sub>=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS}$ =0V $I_D$ =250 $\mu$ A	30	33	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =30 $V$ , $V_{GS}$ =0 $V$	-	-	1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20 $V$ , $V_{DS}$ =0 $V$	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}$ = $V_{GS}$ , $I_D$ =250 $\mu$ A	1	1.6	3	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =10V, $I_D$ =6A	-	20	30	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =6A	15	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C <sub>lss</sub>	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V,	-	255	-	PF
Output Capacitance	C <sub>oss</sub>	F=1.0MHz	-	45	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	T = 1.01VII 12	-	35	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	4.5	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =15 $V$ , $R_L$ =2.5 $\Omega$	-	2.5	-	nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =10 $V$ , $R_{GEN}$ =3 $\Omega$	-	14.5	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	3.5	-	nS
Total Gate Charge	Qg	\/ -15\/   -6^	-	13	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =15V, $I_{D}$ =6A, $V_{GS}$ =10V	-	5.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	v GS-10 v	-	3.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =6A	-	8.0	1.2	V

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# P-CH Electrical Characteristics (T<sub>A</sub>=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS}$ =0 $V$ $I_D$ =-250 $\mu$ A	-30	-33	1	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =-30 $V$ , $V_{GS}$ =0 $V$	-	-	-1	μΑ	
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20 $V$ , $V_{DS}$ =0 $V$	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}$ = $V_{GS}$ , $I_D$ =-250 $\mu A$	-1.5	-1.9	-2.5	V	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =-10V, $I_D$ =-6.5A	-	28	33	mΩ	
Forward Transconductance	<b>g</b> FS	$V_{DS}$ =-5 $V$ , $I_{D}$ =-6.5 $A$	10	-	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	C <sub>lss</sub>	$V_{DS}$ =-15V, $V_{GS}$ =0V,	-	520	-	PF	
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =-15V,V <sub>GS</sub> =UV, F=1.0MHz	-	100	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>	1 – 1.0IVII 12	-	65	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t <sub>d(on)</sub>		-	7.5	-	nS	
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-15V, $R_L$ =2.3 $\Omega$	-	5.5	-	nS	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =-10 $V$ , $R_{GEN}$ =6 $\Omega$	-	19	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	7	-	nS	
Total Gate Charge	Qg	\/ - 15\/   - 6.5 \	-	9.2	-	nC	
Gate-Source Charge	$Q_{gs}$	$V_{DS}$ =-15V, $I_{D}$ =-6.5A $V_{GS}$ =-10V	-	1.6	-	nC	
Gate-Drain Charge	$Q_{gd}$	VGS10V	-	2.2	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-6.5A	-	-	-1.2	V	

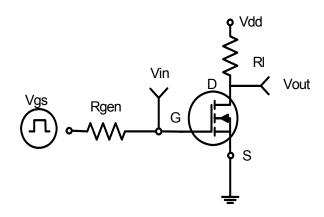
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production

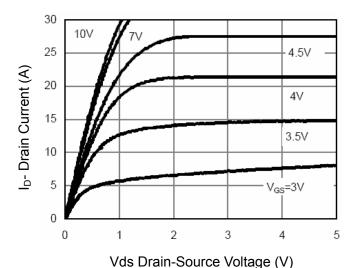
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## N- Channel Typical Electrical and Thermal Characteristics (Curves)



**Figure 1:Switching Test Circuit** 



**Figure 3 Output Characteristics** 

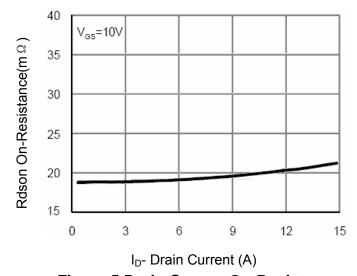
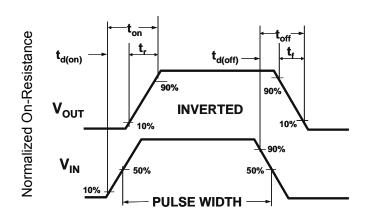
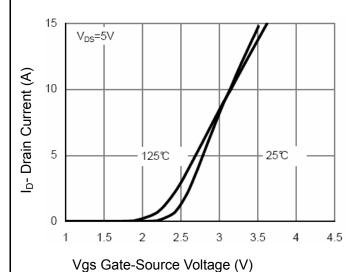


Figure 5 Drain-Source On-Resistance



**Figure 2:Switching Waveforms** 



**Figure 4 Transfer Characteristics** 

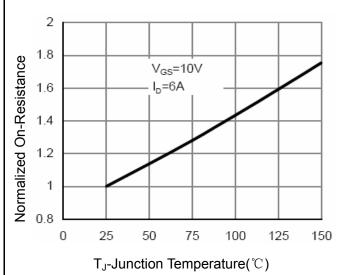


Figure 6 Drain-Source On-Resistance



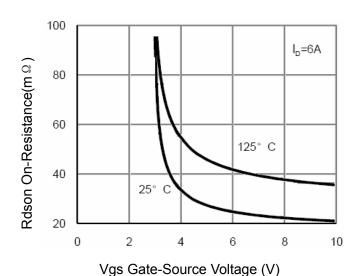


Figure7 Rdson vs Vgs

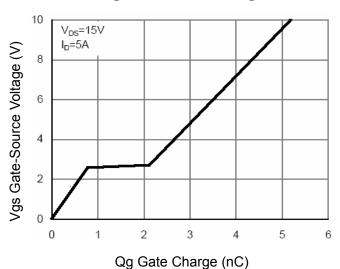


Figure 9 Gate Charge

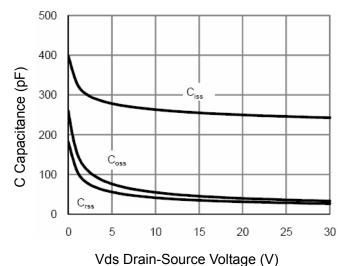


Figure 11 Capacitance vs Vds

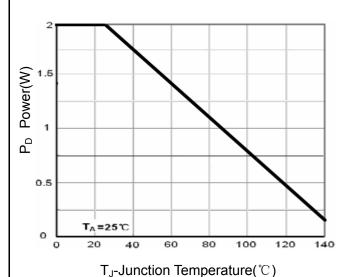


Figure 8 Power Dissipation

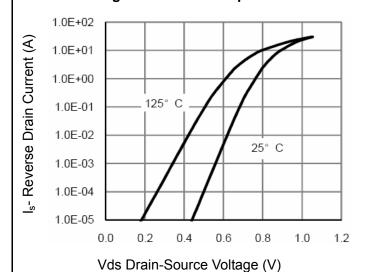
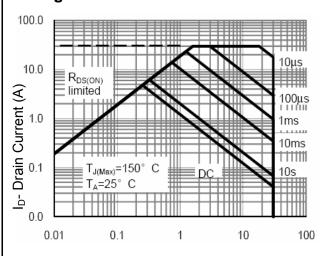
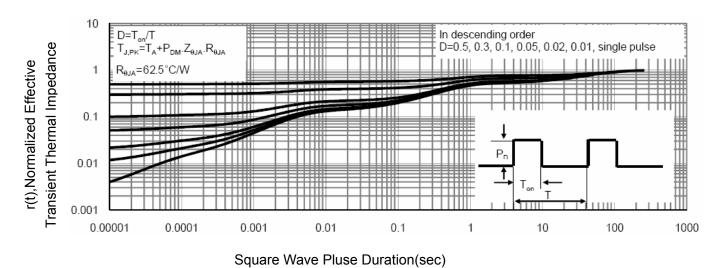


Figure 10 Source- Drain Diode Forward



Vds Drain-Source Voltage (V)
Figure 12 Safe Operation Area





**Figure 13 Normalized Maximum Transient Thermal Impedance** 



## P- Channel Typical Electrical and Thermal Characteristics (Curves)

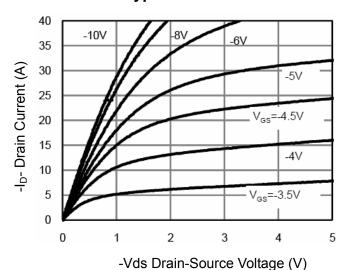
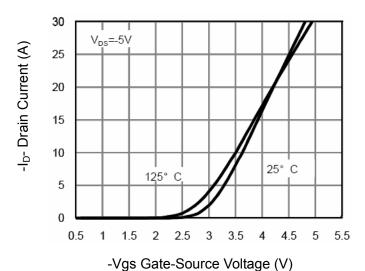


Figure 1 Output Characteristics



**Figure 2 Transfer Characteristics** 

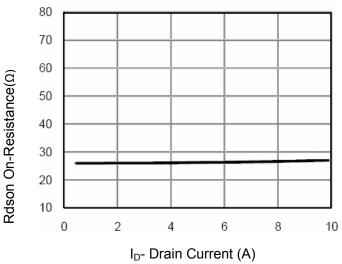
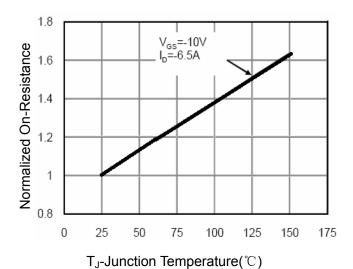


Figure 3 Rdson- Drain Current



**Figure 4 Rdson-Junction Temperature** 

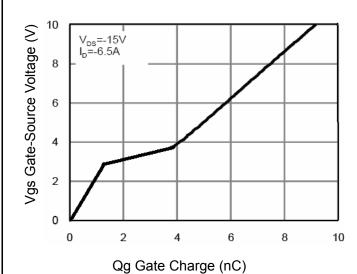


Figure 5 Gate Charge

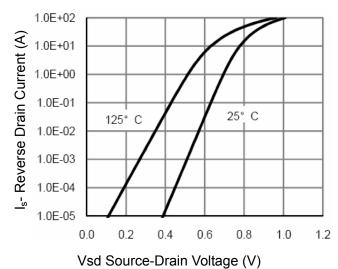


Figure 6 Source- Drain Diode Forward



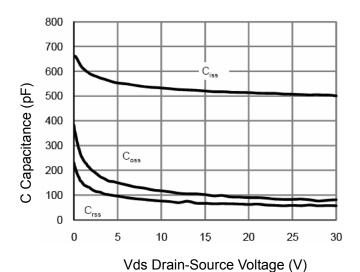


Figure 7 Capacitance vs Vds

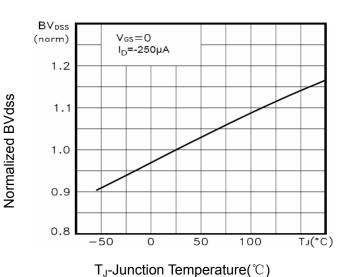
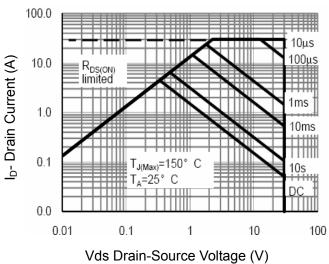


Figure 9 BV<sub>DSS</sub> vs Junction Temperature



**Figure 8 Safe Operation Area** 

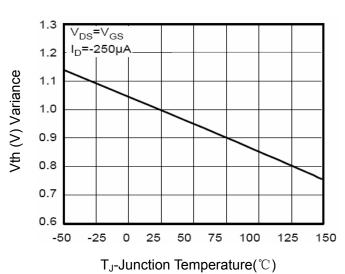
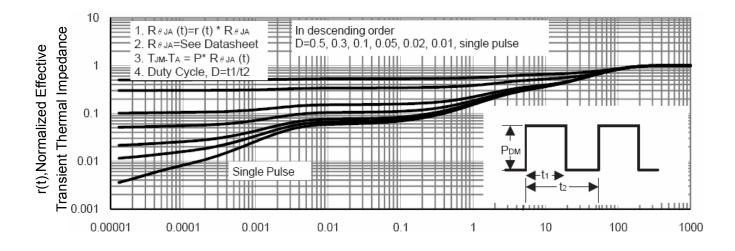


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature



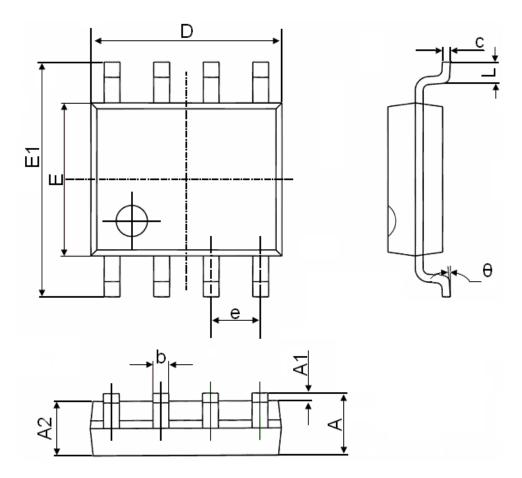
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

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# **SOP-8 Package Information**



Cumbal	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	(BSC)	0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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NCE4606

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