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NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE2305 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

General Features

• $V_{DS} = -20V, I_{D} = -4.1A$

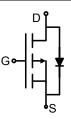
 $R_{DS(ON)}$ <75m Ω @ V_{GS} =-2.5V

 $R_{DS(ON)}$ < 52m Ω @ V_{GS} =-4.5V

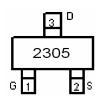
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOT-23 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2305	NCE2305	SOT-23	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25 ℃ unless otherwise noted)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	-20	V		
Gate-Source Voltage		V _G s	±12	V	
	T _C =25°C		-4.1	Α	
Continuous Drain Current	T _C =70°C	ı	-3.2		
Continuous Drain Current	T _A =25℃	- I _D	-3		
	T _A =70°C		-2.3		
Drain Current -Pulsed (Note 1)		I _{DM}	-15	Α	
Maximum Power Dissipation		P _D	1.7	W	
Operating Junction and Storage Temperature Range		T_{J}, T_{STG}	-55 To 150	$^{\circ}$ C	

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	74	°C/W
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Electrical Characteristics (T_A=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·					
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-20	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V,V _{GS} =0V	-	-	-1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)				•		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250μA	-0.45	-0.7	-1.0	V
Danier Courses On Otata Basintana	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-4.1A	-	39	52	mΩ
Drain-Source On-State Resistance		V _{GS} =-2.5V, I _D =-3A	-	58	75	
Forward Transconductance	g FS	V _{DS} =-5V,I _D =-2A	6	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ - 4\/\/ -0\/	-	740	-	PF
Output Capacitance	C _{oss}	V_{DS} =-4V, V_{GS} =0V, F=1.0MHz	-	290	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVITZ	-	190	-	PF
Switching Characteristics (Note 4)	·					
Turn-on Delay Time	t _{d(on)}		-	12	-	nS
Turn-on Rise Time	t _r	V_{DD} =-4 V , I_{D} =-3.3 A ,	-	35	-	nS
Turn-Off Delay Time	$t_{d(off)}$	R_L =-1.2 Ω , V_{GEN} =-4.5 V , R_g =1 Ω	-	30	-	nS
Turn-Off Fall Time	t _f]	-	10	ı	nS
Total Gate Charge	Qg		-	7.8	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =-4V, I_{D} =-4.1A, V_{GS} =-4.5V	-	1.2	-	nC
Gate-Drain Charge	Q_{gd}]	-	1.6	ı	nC
Drain-Source Diode Characteristics	•	•	•	•		•
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =-1.6A	-	-	-1.2	V
Diode Forward Current (Note 2)	I _S		-	-	1.6	Α

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width ≤ 300 μ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

Pb Free Product



Typical Electrical and Thermal Characteristics

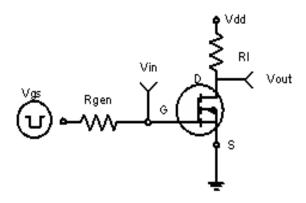


Figure 1:Switching Test Circuit

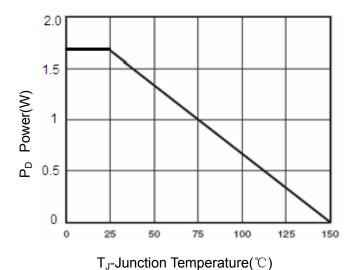


Figure 3 Power Dissipation

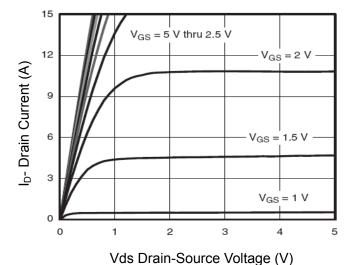


Figure 5 Output Characteristics

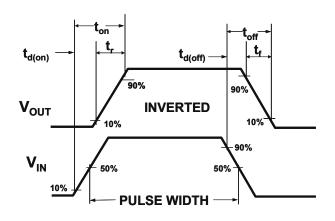
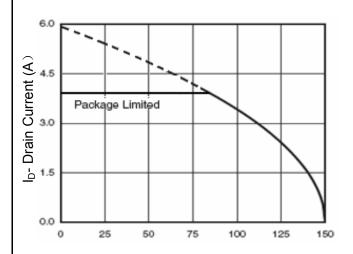


Figure 2:Switching Waveforms



T_J-Junction Temperature(°C)

Figure 4 Drain Current

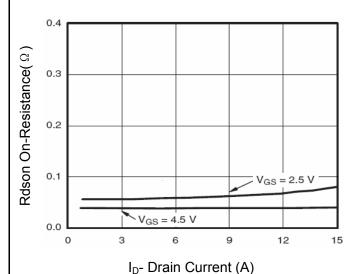
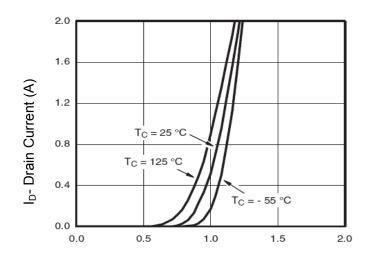


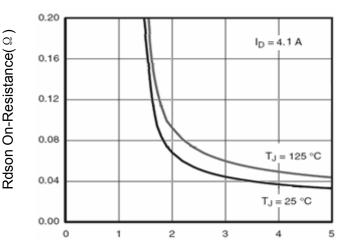
Figure 6 Drain-Source On-Resistance





Vgs Gate-Source Voltage (V)

Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs

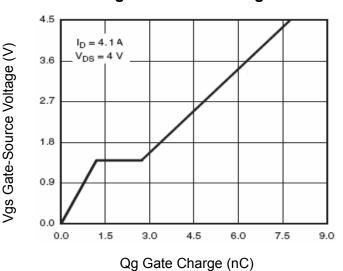


Figure 11 Gate Charge

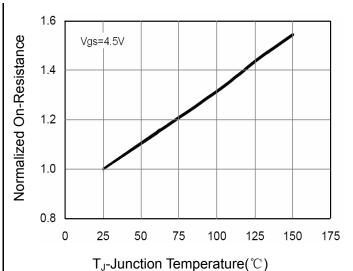
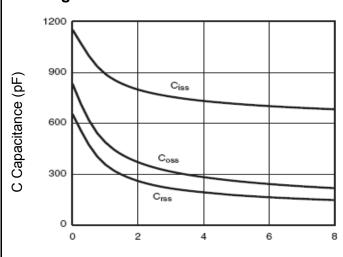
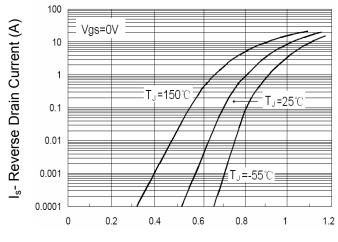


Figure 8 Drain-Source On-Resistance



Vds Drain-Source Voltage (V)

Figure 10 Capacitance vs Vds



Vsd Source-Drain Voltage (V)

Figure 12 Source- Drain Diode Forward



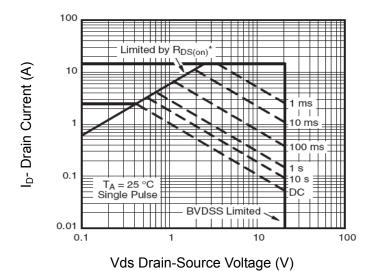


Figure 13 Safe Operation Area

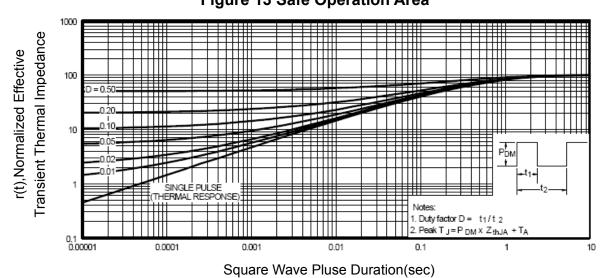
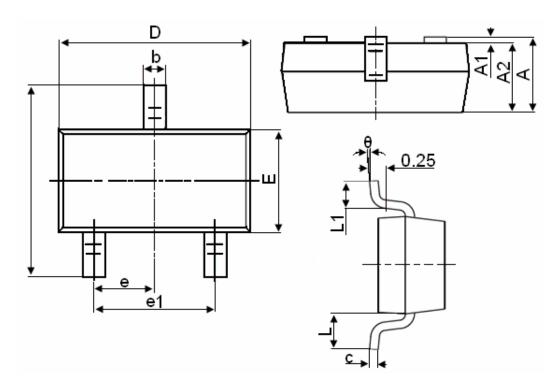


Figure 14 Normalized Maximum Transient Thermal Impedance



SOT-23 Package Information



Symbol	Dimensions in Millimeters			
	MIN.	MAX.		
Α	0.900	1.150		
A1	0.000	0.100		
A2	0.900	1.050		
b	0.300	0.500		
С	0.080	0.150		
D	2.800	3.000		
E	1.200	1.400		
E1	2.250	2.550		
е	0.950TYP			
e1	1.800	2.000		
L	0.550REF			
L1	0.300	0.500		
θ	0°	8°		

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- $5. \ Controlling \ dimension \ is \ millimeter, \ converted \ inch \ dimensions \ are \ not \ necessarily \ exact.$



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NCE2305

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