



NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE2011E uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.It is ESD protested.

General Features

• $V_{DS} = 20V, I_{D} = 11A$

 $R_{DS(ON)}$ < 7m Ω @ V_{GS} =2.5V

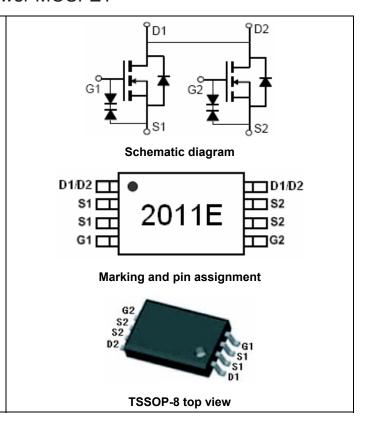
 $R_{DS(ON)} < 9m\Omega$ @ V_{GS} =4.5V

ESD Rating: 2000V HBM

- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- PWM application
- Load switch



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2011E	NCE2011E	TSSOP-8	Ø330mm	12mm	3000 units

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±10	V
Drain Current-Continuous	I _D	11	Α
Drain Current-Pulsed (Note 1)	I _{DM}	44	Α
Maximum Power Dissipation	P _D	1.6	W
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 150	$^{\circ}\!$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	78	°C/W
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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	20		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V,V _{GS} =0V	-	-	1	μΑ



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Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±10V,V _{DS} =0V	-	-	±10	μA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250\mu A$	0.6	0.8	1.2	V
Drain Course On Ctata Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =10A	-	5.5	7	mΩ
Drain-Source On-State Resistance		V _{GS} =2.5V, I _D =5.5A	-	7	9	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =11A	25	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ -40\/\/ -0\/	-	1710	-	PF
Output Capacitance	Coss	V_{DS} =10V, V_{GS} =0V, F=1.0MHz	-	232	-	PF
Reverse Transfer Capacitance	C _{rss}	r=1.0ivin2	-	200	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	2.5		nS
Turn-on Rise Time	t _r	V_{DD} =10 V , R_L =1 Ω	-	7.2		nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{GEN} =3 Ω	-	49		nS
Turn-Off Fall Time	t _f		-	10.8		nS
Total Gate Charge	Qg	V _{DS} =10V,I _D =10A, V _{GS} =4.5V	-	17.5		nC
Gate-Source Charge	Q _{gs}		-	1.5	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} -4.5V	-	4.5	-	nC
Drain-Source Diode Characteristics						•
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =1A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	11	Α

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

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Typical Electrical and Thermal Characteristics

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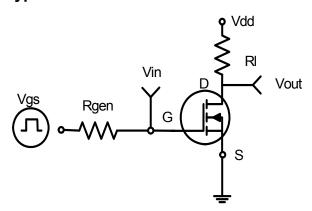


Figure 1:Switching Test Circuit

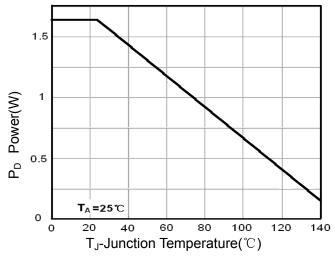


Figure 3 Power Dissipation

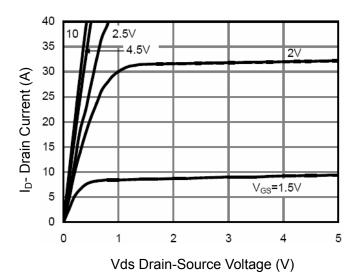


Figure 5 Output Characteristics

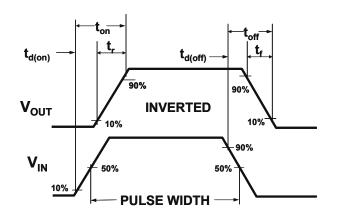


Figure 2:Switching Waveforms

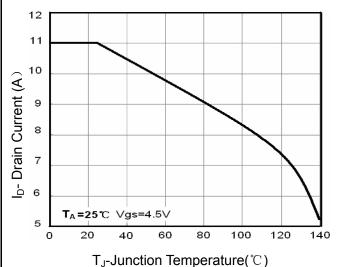


Figure 4 Drain Current

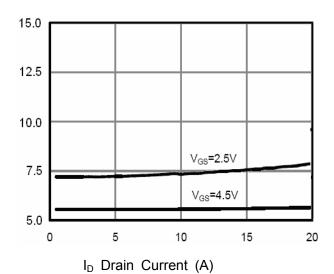


Figure 6 Drain-Source On-Resistance



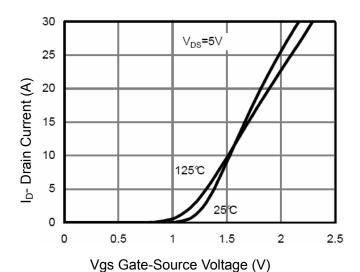


Figure 7 Transfer Characteristics

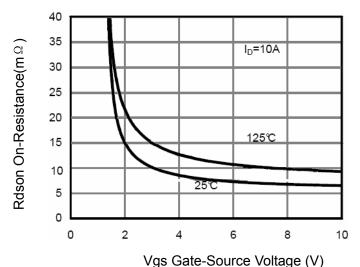


Figure 9 Rdson vs Vgs

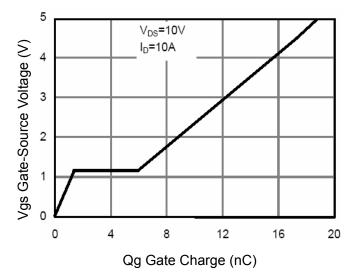


Figure 11 Gate Charge

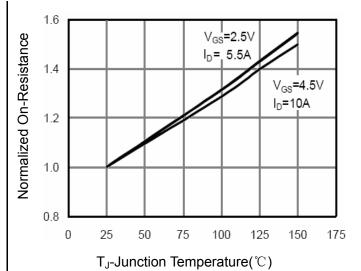


Figure 8 Drain-Source On-Resistance

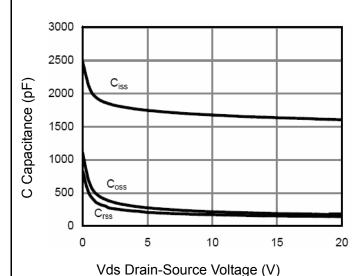


Figure 10 Capacitance vs Vds

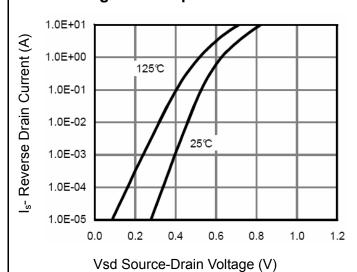


Figure 12 Source- Drain Diode Forward



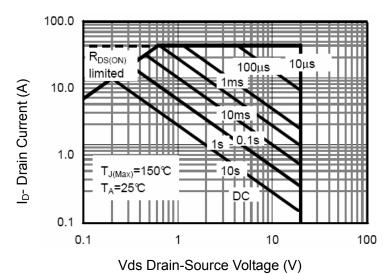


Figure 13 Safe Operation Area

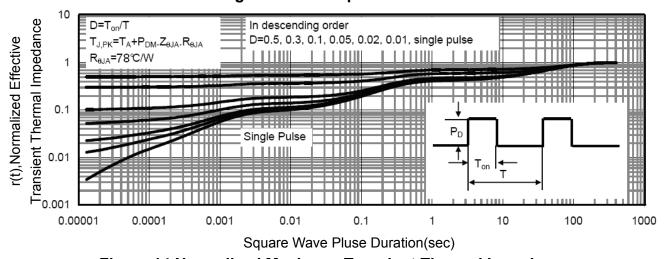
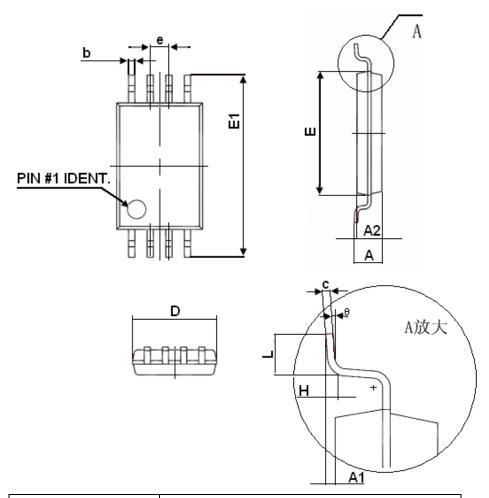


Figure 14 Normalized Maximum Transient Thermal Impedance

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Tssop-8 Package Information



Symbol	Dimensions In Millimeters			
Symbol	Min	Max		
D	2.900	3.100		
E	4.300	4.500		
b	0.190	0.300		
С	0.090	0.200		
E1	6.250	6.550		
Α		1.100		
A2	0.800	1.000		
A 1	0.020	0.150		
е	0.65(BSC)			
L	0.500	0.700		
Н	0.25(TYP)			
Θ	1° 7°			



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