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# NCE0208IA

#### NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE0208IA uses advanced trench technology and design to provide excellent R<sub>DS(ON)</sub> with low gate charge. It can be used in a wide variety of applications.

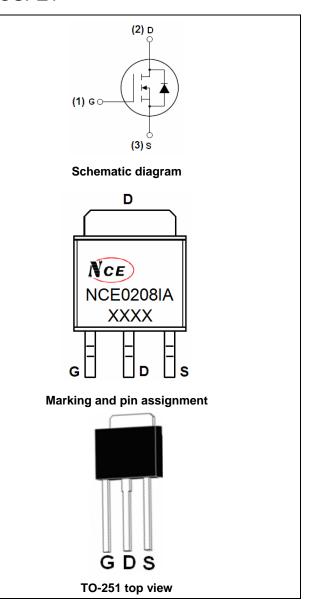
#### **General Features**

- V<sub>DS</sub> =200V,I<sub>D</sub> =8A  $R_{DS(ON)}$  <300m $\Omega$  @  $V_{GS}$ =10V (Typ: 260m $\Omega$ )
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Low gate to drain charge to reduce switching losses

#### **Application**

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% ΔVds TESTED!



#### Package Marking and Ordering Information

<b>Device Marking</b>	Device	Device Package	Reel Size	Tape width	Quantity
NCE0208IA	NCE0208IA	TO-251	-	-	-

#### Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	200	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
Drain Current-Continuous	I <sub>D</sub>	8	Α
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	5.6	Α
Pulsed Drain Current	I <sub>DM</sub>	20	А
Maximum Power Dissipation	P <sub>D</sub>	55	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}\!\mathbb{C}$

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# NCE0208IA

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case (Note 2)	R <sub>θJC</sub>	2.3	°C/W
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## Electrical Characteristics (T<sub>C</sub>=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS}$ =0 $V$ $I_D$ =250 $\mu$ A	V <sub>GS</sub> =0V I <sub>D</sub> =250μA 200		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =200 $V$ , $V_{GS}$ =0 $V$	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1	1.7	2.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =10V, $I_D$ =4.5A	-	260	300	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =25V,I <sub>D</sub> =4.5A	3	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ -25\/\/ -0\/		540		PF
Output Capacitance	Coss	$V_{DS}$ =25V, $V_{GS}$ =0V, F=1.0MHz		90		PF
Reverse Transfer Capacitance	C <sub>rss</sub>	r-1.0WInz		35		PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	6.4	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =100 $V$ , $I_{D}$ =4.5 $A$	-	11	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{GEN}$ =5 $\Omega$	-	20	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Qg	.,	-	16	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =160V,I <sub>D</sub> =4.5A,	-	3.4	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	5.1	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =4.5A	-	-	1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	8	Α

#### Notes:

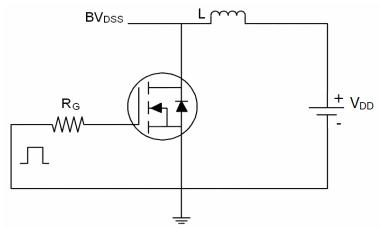
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

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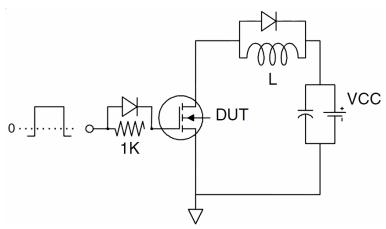


#### **Test Circuit**

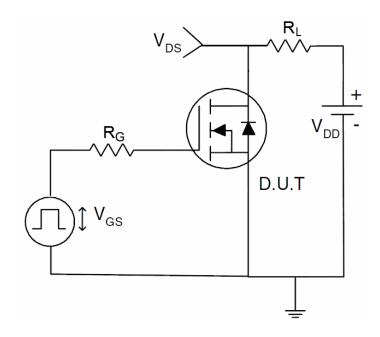
## 1) E<sub>AS</sub> test Circuit



### 2) Gate charge test Circuit



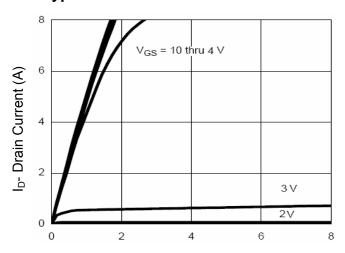
#### 3) Switch Time Test Circuit



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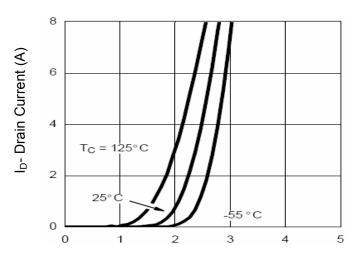


#### **Typical Electrical and Thermal Characteristics (Curves)**



Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics



Vgs Gate-Source Voltage (V)

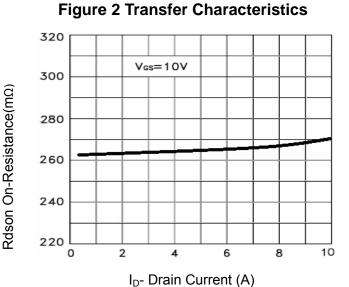


Figure 3 Rdson- Drain Current

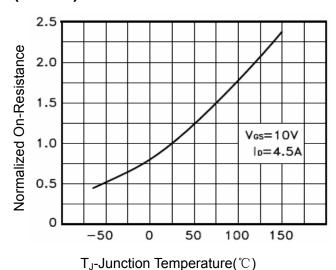


Figure 4 Rdson-JunctionTemperature

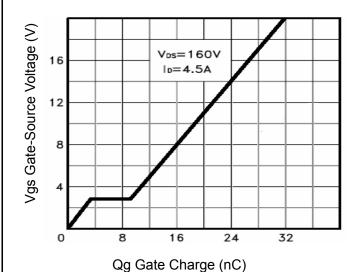


Figure 5 Gate Charge

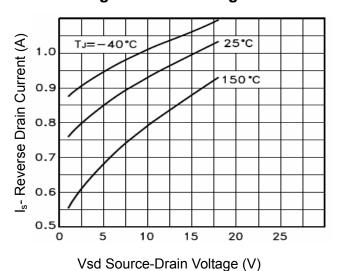


Figure 6 Source- Drain Diode Forward



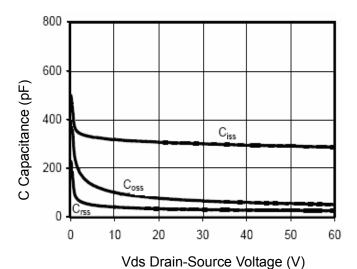
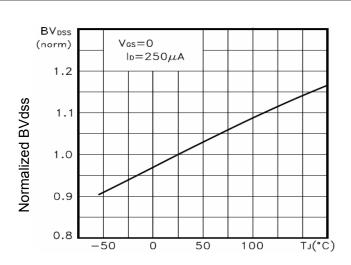
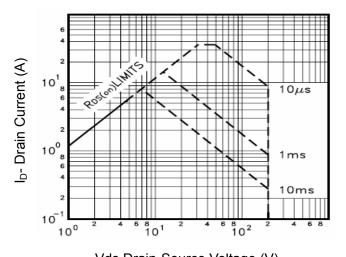


Figure 7 Capacitance vs Vds

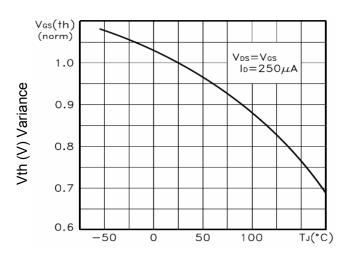


 $T_J$ -Junction Temperature (°C) Figure 9 BV<sub>DSS</sub> vs Junction Temperature



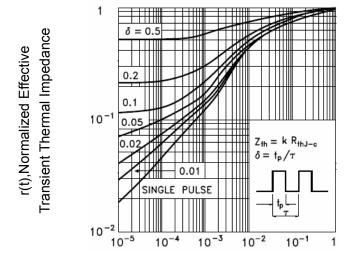
Vds Drain-Source Voltage (V)

**Figure 8 Safe Operation Area** 



T<sub>J</sub>-Junction Temperature(°C)

Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

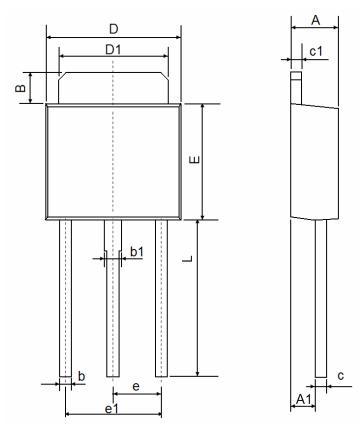


Square Wave Pluse Duration(sec)

**Figure 11 Normalized Maximum Transient Thermal Impedance** 



# **TO-251 Package Information**



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	1.050	1.350	0.042	0.054	
В	1.350	1.650	0.053	0.065	
b	0.500	0.700	0.020	0.028	
b1	0.700	0.900	0.028	0.035	
С	0.430	0.580	0.017	0.023	
c1	0.430	0.580	0.017	0.023	
D	6.350	6.650	0.250	0.262	
D1	5.200	5.400	0.205	0.213	
E	5.400	5.700	0.213	0.224	
е	2.300 TYP		0.091 TYP		
e1	4.500	4.700	0.177	0.185	
L	7.500	7.900	0.295	0.311	



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