



#### NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

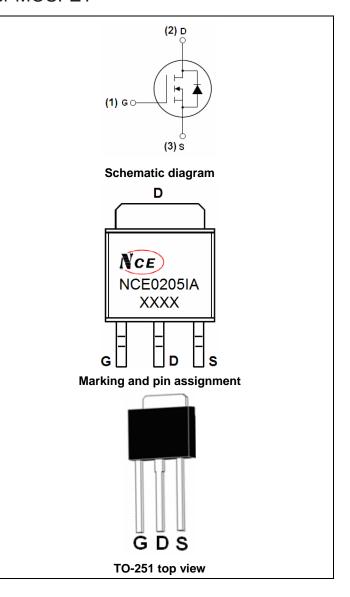
The NCE0205IA uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

- $V_{DS}$  = 200V, $I_{D}$  =5A  $R_{DS(ON)} < 580$ mΩ @  $V_{GS}$ =10V (Typ:520mΩ)
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

#### **Application**

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



## **Package Marking and Ordering Information**

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Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE0205IA	NCE0205IA	TO-251	-	-	-

## Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	200	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	5	Α
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	20	Α
Maximum Power Dissipation	P <sub>D</sub>	30	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$ C



## http://www.ncepower.com

# NCE0205IA

## **Thermal Characteristic**

Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

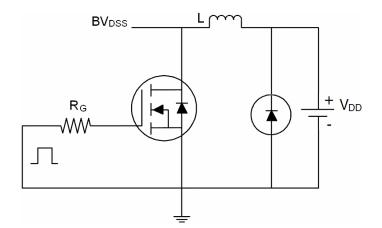
Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	200	-	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =200V,V <sub>GS</sub> =0V	-	-	1	μA	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.2	1.7	2.5	V	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =2A	-	520	580	mΩ	
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =15V,I <sub>D</sub> =2A	-	8	-	S	
Dynamic Characteristics (Note4)			•				
Input Capacitance	C <sub>lss</sub>	\/ -05\/\/ -0\/	-	580	-	PF	
Output Capacitance	Coss	$V_{DS}$ =25V, $V_{GS}$ =0V,	-	90	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	3	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t <sub>d(on)</sub>		-	10	-	nS	
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =100V, $R_L$ =15 $\Omega$	-	12	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =2.5 $\Omega$	-	15	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	15	-	nS	
Total Gate Charge	Qg	\/ -400\/   -24	-	12		nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}=100V, I_{D}=2A,$	-	2.5	-	nC	
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	3.8	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =2A	-	-	1.2	V	
Diode Forward Current (Note 2)	Is		-	-	5	Α	

#### Notes:

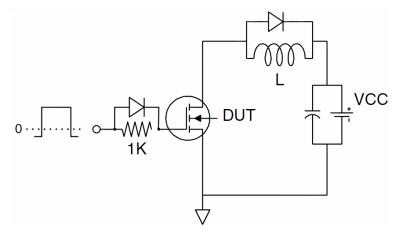
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

## **Test Circuit**

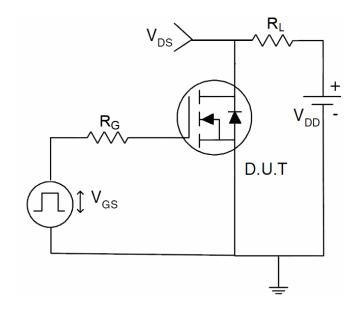
## 1) E<sub>AS</sub> test circuit



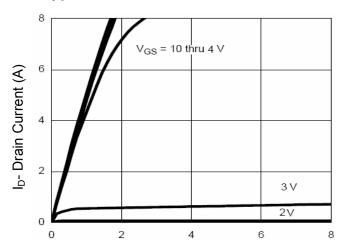
## 2) Gate charge test circuit



#### 3) Switch Time Test Circuit

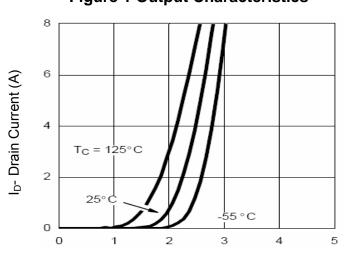


## **Typical Electrical and Thermal Characteristics (Curves)**



Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics



Vgs Gate-Source Voltage (V)
Figure 2 Transfer Characteristics

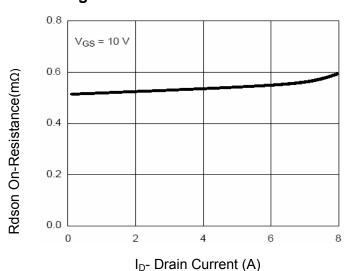
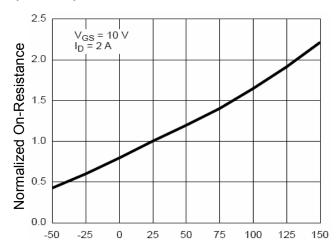
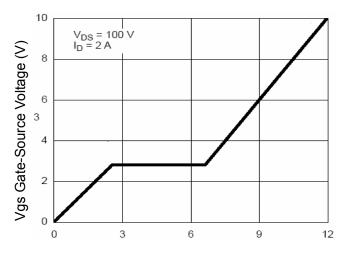


Figure 3 Rdson- Drain Current

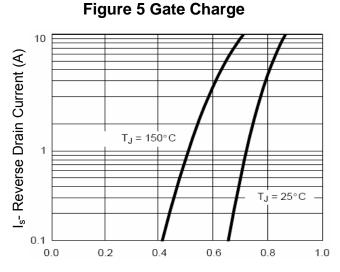


 $\mathsf{T}_{\mathsf{J}} ext{-Junction Temperature}({}^{\mathbb{C}})$ 

## Figure 4 Rdson-JunctionTemperature



Qg Gate Charge (nC)



Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward

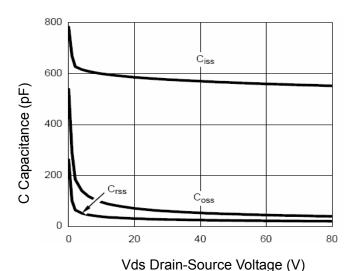
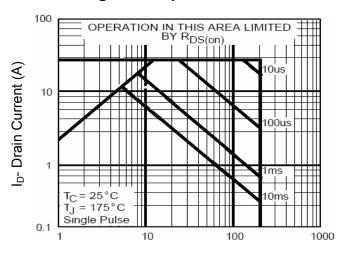
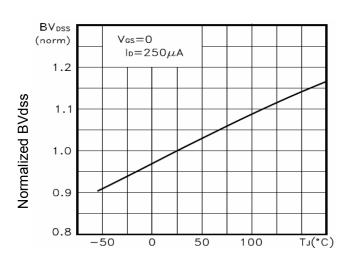


Figure 7 Capacitance vs Vds

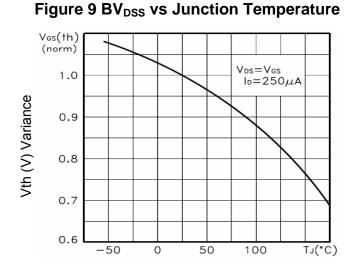


Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area

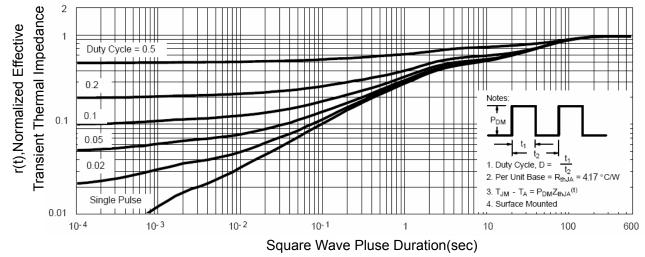


 $T_J$ -Junction Temperature( ${}^{\circ}\mathbb{C}$ )



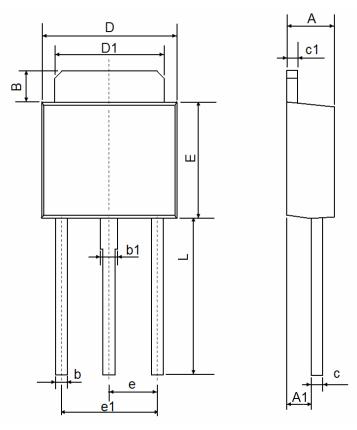
 $T_J$ -Junction Temperature( ${}^{\circ}\mathbb{C}$ )

Figure 10 V<sub>GS(th)</sub> vs Junction Temperature



**Figure 11 Normalized Maximum Transient Thermal Impedance** 

## **TO-251 Package Information**



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	1.050	1.350	0.042	0.054	
В	1.350	1.650	0.053	0.065	
b	0.500	0.700	0.020	0.028	
b1	0.700	0.900	0.028	0.035	
С	0.430	0.580	0.017	0.023	
c1	0.430	0.580	0.017	0.023	
D	6.350	6.650	0.250	0.262	
D1	5.200	5.400	0.205	0.213	
Е	5.400	5.700	0.213	0.224	
е	2.300 TYP		0.091 TYP		
e1	4.500	4.700	0.177	0.185	
L	7.500	7.900	0.295	0.311	

**Pb-Free Product** 

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