# MBRS1635 THRU MBRS1660

## SCHOTTKY BARRIER RECTIFIER

### REVERSE VOLTAGE: FORWARD CURRENT:

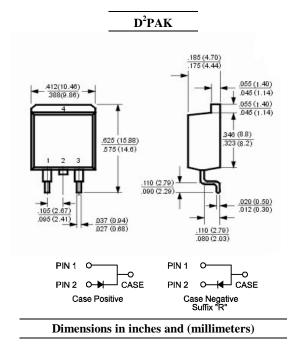
## 35 to 60 VOLTS 16.0 AMPERE

### FEATURES

- $\cdot$  For surface mounted application
- $\cdot$  Metal silicon junction, majority carrier conduction
- $\cdot$  Guard ring for overvoltage protection
- $\cdot$  Low power loss, high efficiency
- For use in low voltage, high frequency inverters, free whelling, and polarity protection applications
- High temperature soldering guaranteed:
  250°C/10 seconds, 0.25" (6.35mm) from case

#### MECHANICAL DATA

Case: Molded plastic, D<sup>2</sup>PAK Epoxy: UL 94V-O rate flame retardant Terminals: Leads solderable per MIL-STD-202 method 208 guaranteed Polarity: As marked Mounting position: Any Weight: 0.06ounce, 1.70gram



#### Maximum Ratings and Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave,  $60H_Z$ , resistive or inductive load. For capacitive load, derate current by 20%.

	Symbols	<b>MBRS1635</b>	<b>MBRS1645</b>	MBRS1650	MBRS1660	Units
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	35	45	50	60	Volts
Maximum RMS Voltage	V <sub>RMS</sub>	24	31	35	42	Volts
Maximum DC Blocking Voltage	V <sub>DC</sub>	20	30	40	50	Volts
Maximum Average Forward Rectified Current at $T_{C} = 125^{\circ}C$	I <sub>(AV)</sub>	16.0				Amp
Peak repetitive forward current at $T_C = 125^{\circ}C$ (rated VR, sq. wave, 20 KHz)	I <sub>FRM</sub>	32.0				Amp
Peak Forward Surge Current, 8.3ms single half-sine-wave superimposed on rated load (JEDEC method)	I <sub>FSM</sub>	150				Amp
Peak repetitive reverse current at tp = 2.0µs, 1KHz	I <sub>RRM</sub>	1.0		0.5		Amp
Maximum Forwardat $I_F = 16A$ , $T_C = 25^{\circ}C$ Voltage (Note 1)at $I_F = 16A$ , $T_C = 125^{\circ}C$	V <sub>F</sub>	0.63 0.57		0.75 0.65		Volts
Maximum Reverse Currentat $T_C=25^{\circ}C$ at Rated DC Blocking Voltage $T_C=125^{\circ}C$	I <sub>R</sub>	0.2 40		1.0 50		mAmp
Voltage rate of change (rated V <sub>R</sub> )	dv/dt	10,000				V/µs
Typical Thermal Resistance	R <sub>0JC</sub>	1.5				°C/W
Operating Temperature Range	T <sub>J</sub>	-55 to +150				Ĉ
Storage Temperature Range	Tstg	-55 to +175				Ĉ

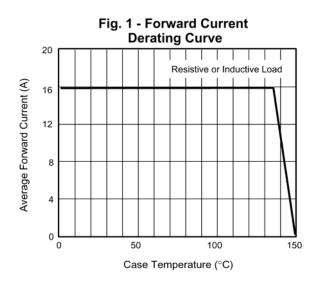
#### NOTES:

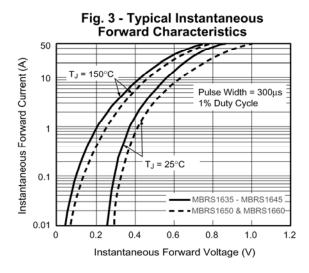
1- Pulse test: 300µs pulse width, 1% duty cycle

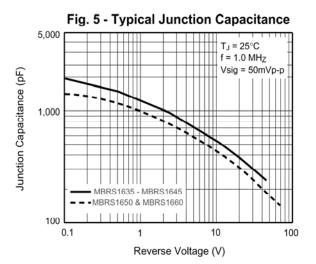


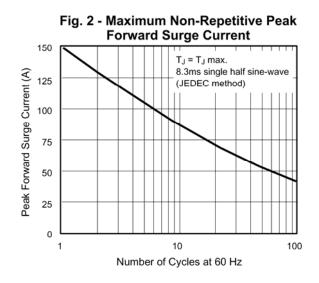
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#### RATINGS AND CHARACTERISTIC CURVES









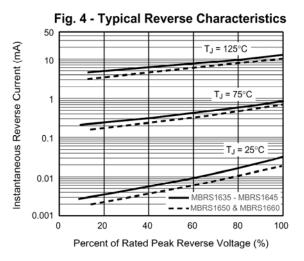


Fig. 6 - Typical Transient Thermal Impedance

