BA157 THRU BA159

FAST RECOVERY RECTIFIER



REVERSE VOLTAGE: 400 to 1000 VOLTS FORWARD CURRENT: 1.0 AMPERE

FEATURES

· High surge current capability

· Void-free Plastic in a DO-41 package.

· Fast switching for high efficiency

· Exceeds environmental standards of MIL-S-19500/228

· Low leakage.

MECHANICAL DATA

Case: Molded plastic, DO-41

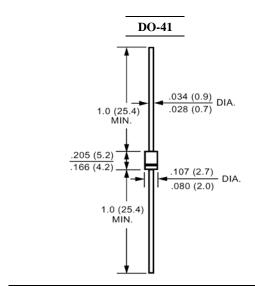
Epoxy: UL 94V-O rate flame retardant

Lead: Axial leads, solderable per MIL-STD-202,

method 208 guaranteed

Polarity: Color band denotes cathode end

Mounting position: Any Weight: 0.012ounce, 0.33gram



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

	Symbols	BA157	BA158	BA159	Units
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	400	600	1000	Volts
Maximum RMS Voltage	V _{RMS}	280	420	700	Volts
Maximum DC Blocking Voltage	V _{DC}	400	600	1000	Volts
Maximum Average Forward Rectified Current	I _(AV)	1.0			Amp
.375"(9.5mm) Lead Length at T _A =50℃					
Peak Forward Surge Current,					
8.3ms single half-sine-wave	I_{FSM}	30			Amp
superimposed on rated load (JEDEC method)					
Maximum Forward Voltage	$V_{\rm F}$ 1.3				Volts
at 1.0A DC and 25℃	' F	1.5			
Maximum Reverse Current at T _A =25℃	T	5.0			
at Rated DC Blocking Voltage T _A =100℃	I_R 50			uAmp	
Typical Junction Capacitance (Note 1)	C_{J}	12			pF
Typical Thermal Resistance (Note 2)	$R_{\theta JA}$	50			°C/W
Maximum Reverse Recovery Time (Note 3)	T_{RR}	1	150	250	nS
Operating and Storage Temperature Range	T _J , Tstg	-55 to +150			°C

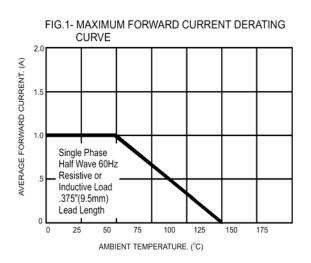
NOTES:

- 1- Measured at 1 MH_Z and applied reverse voltage of 4.0 VDC.
- 2- Thermal Resistance From Junction to Ambient 0.375" (9.5mm) lead length P.C.B. Mounted.
- 3- Reverse Recovery Test Conditions: I_F =.5A, I_R =1A, I_{RR} =.25A.





RATINGS AND CHARACTERISTIC CURVES



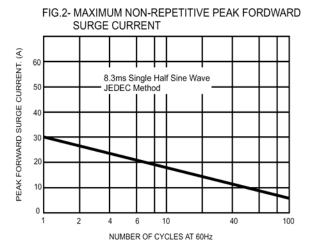


FIG.3- TYPICAL FORWARD CHARACTERISTICS

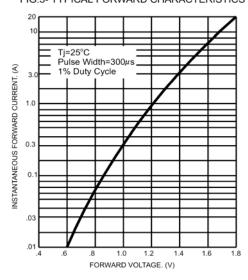


FIG.4- TYPICAL JUNCTION CAPACITANCE

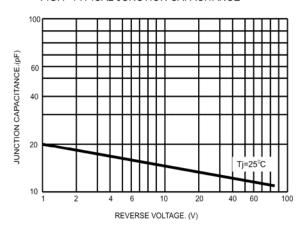


FIG.5- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

